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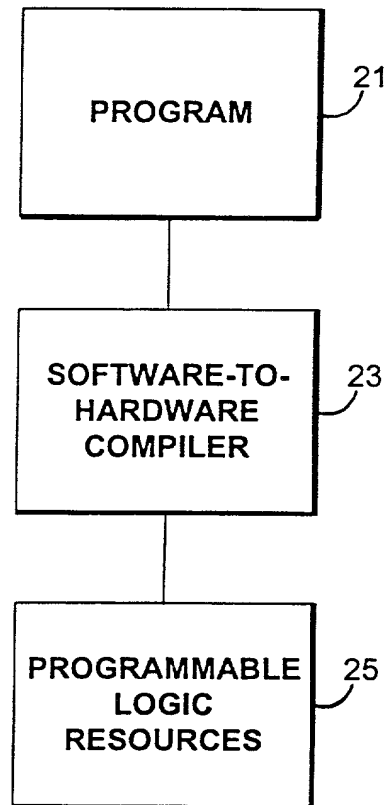


FIG. 1

2 / 5 2

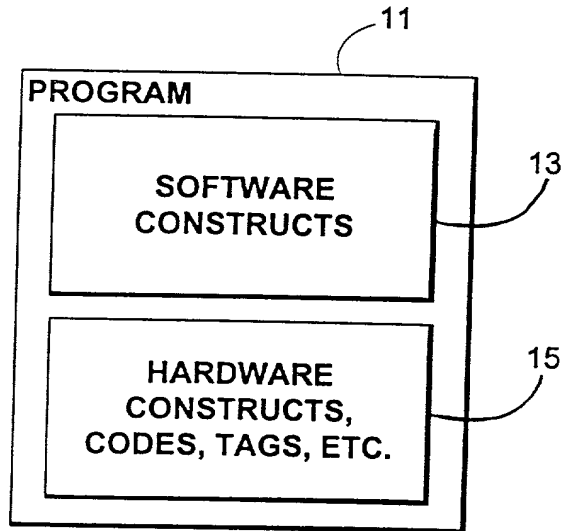


FIG. 2
(PRIOR ART)

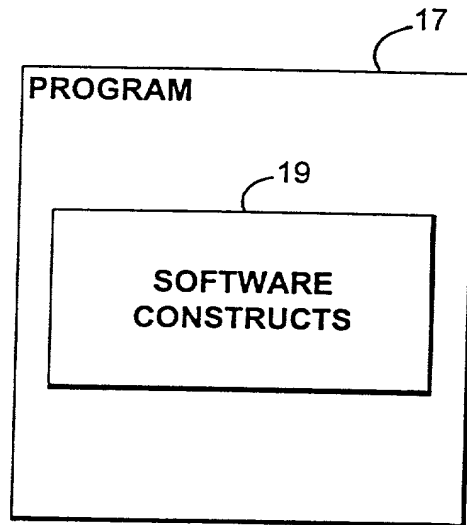


FIG. 3

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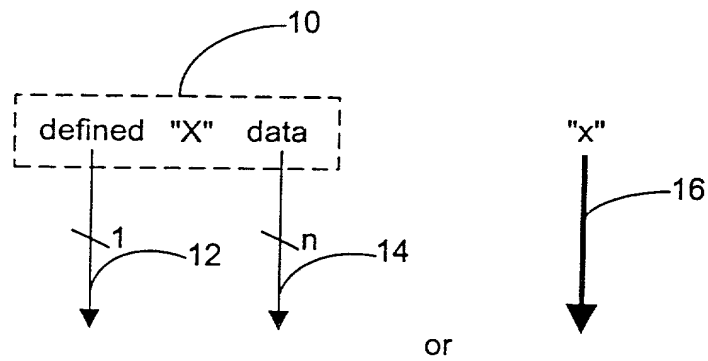


FIG. 4

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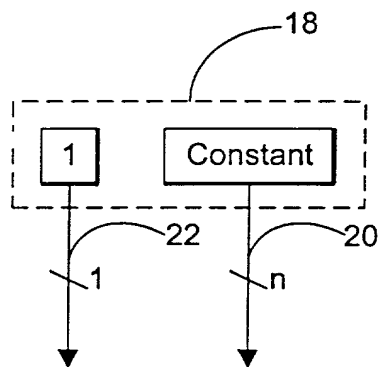


FIG. 5

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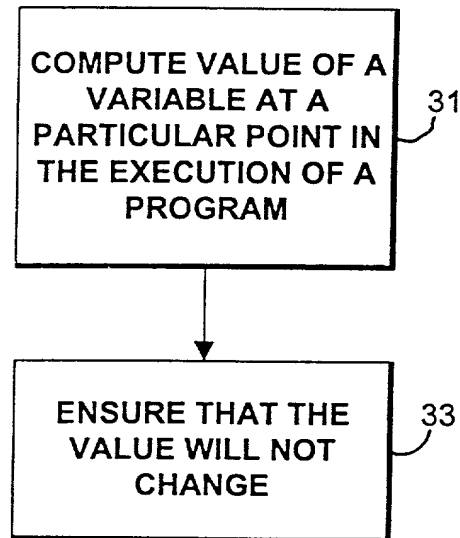


FIG. 6

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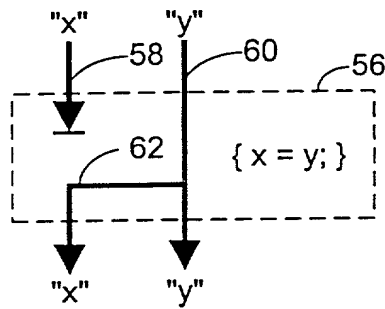


FIG. 7

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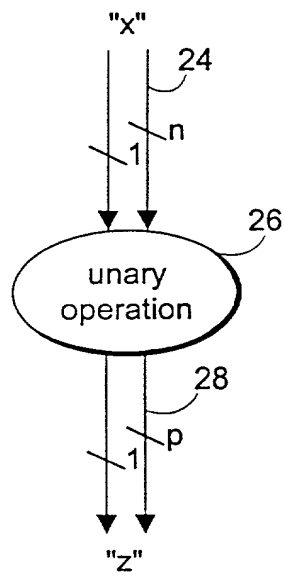


FIG. 8

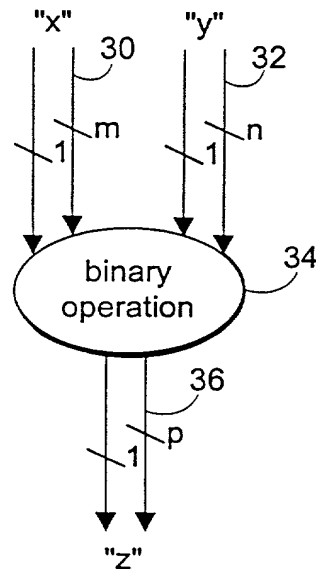


FIG. 9

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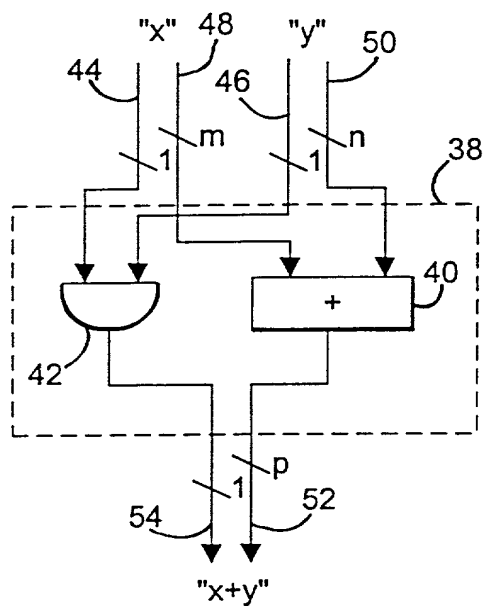


FIG. 10

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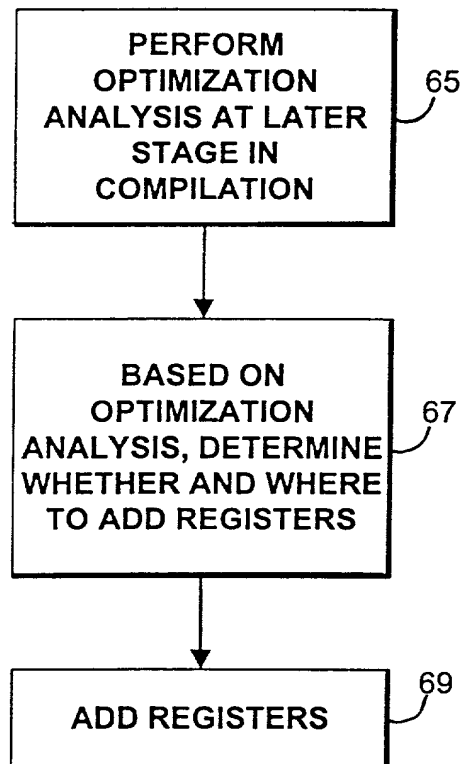


FIG. 11

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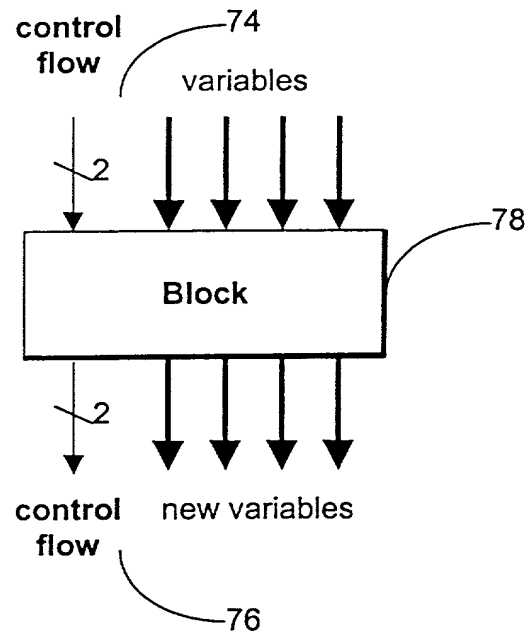


FIG. 12

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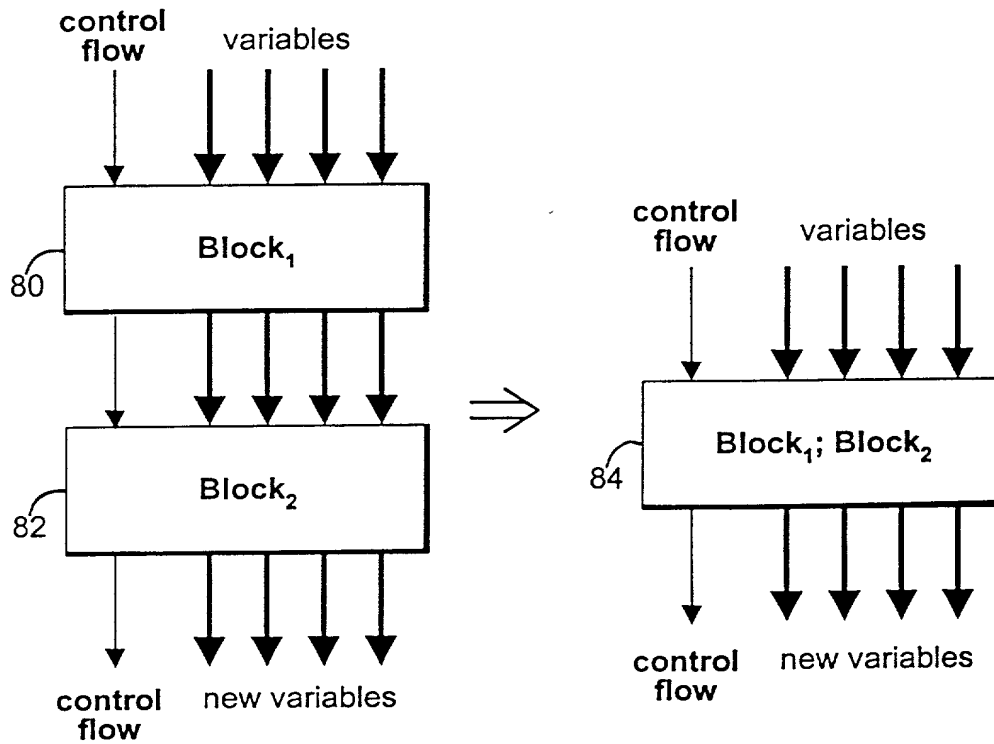


FIG. 13

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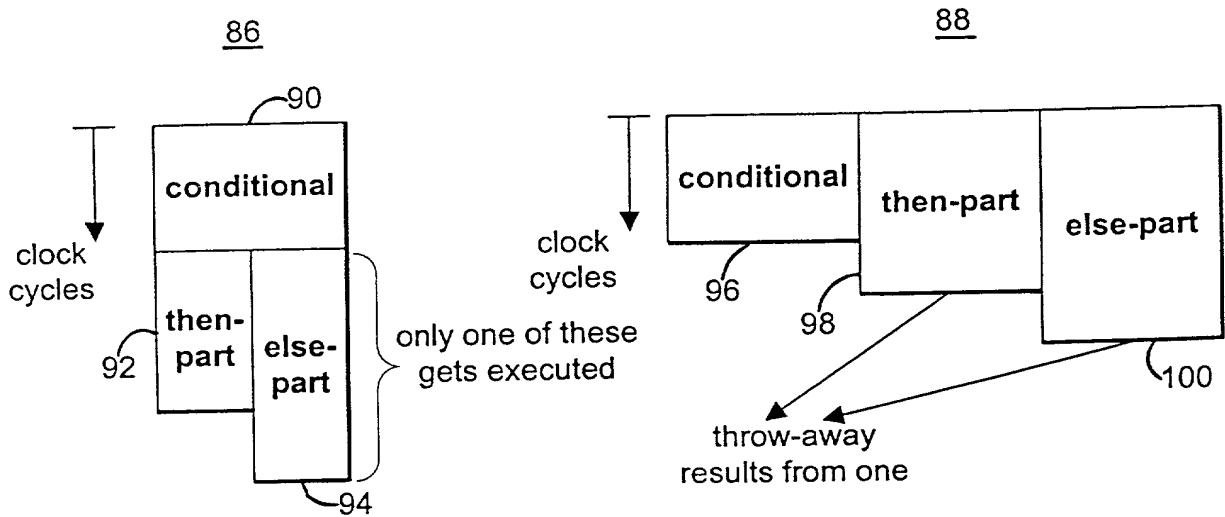


FIG. 14

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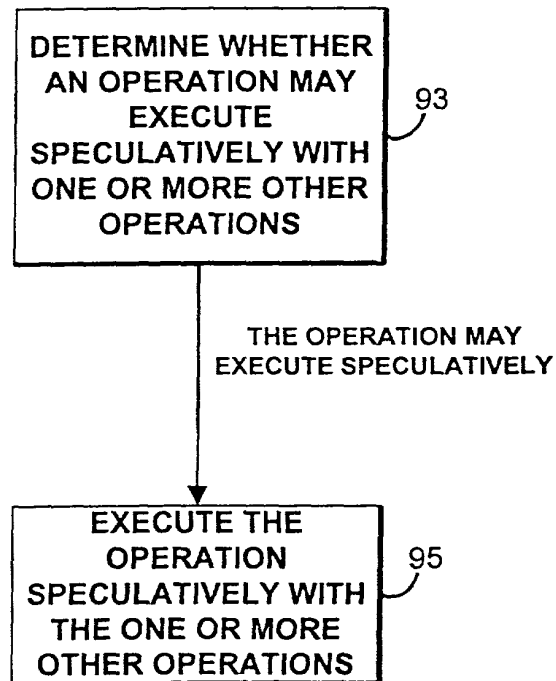


FIG. 15

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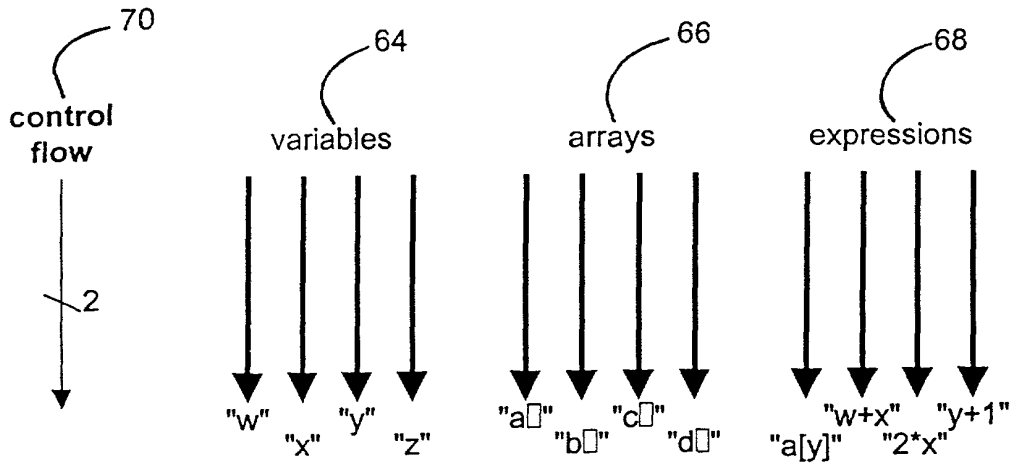


FIG. 16

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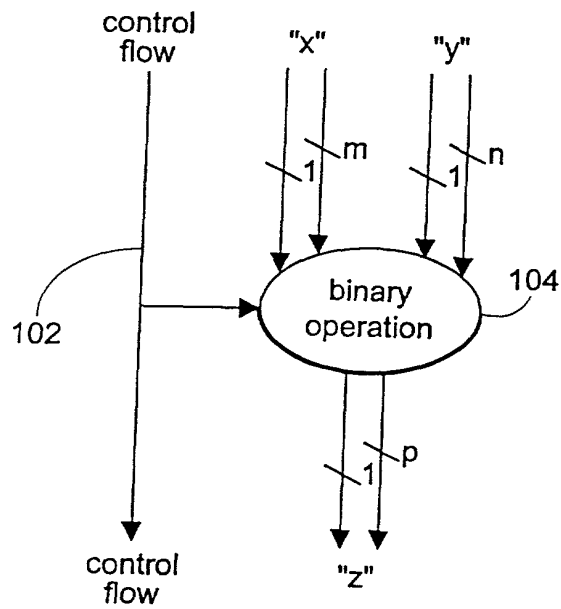


FIG. 17

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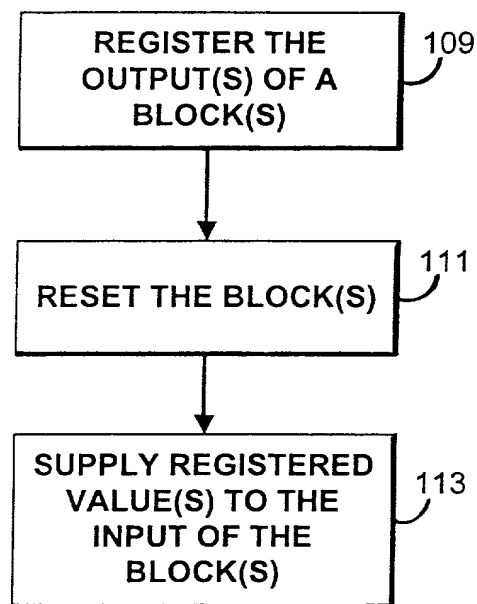


FIG. 18

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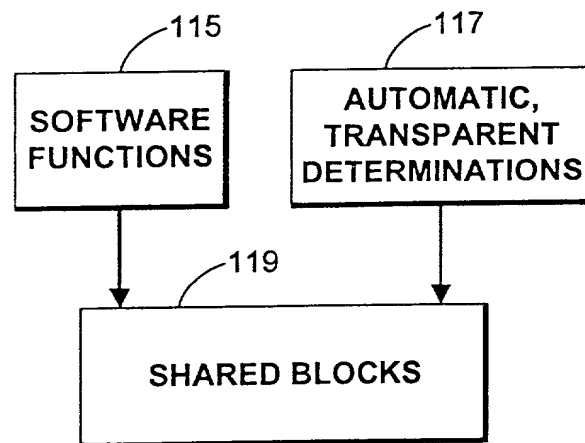


FIG. 19

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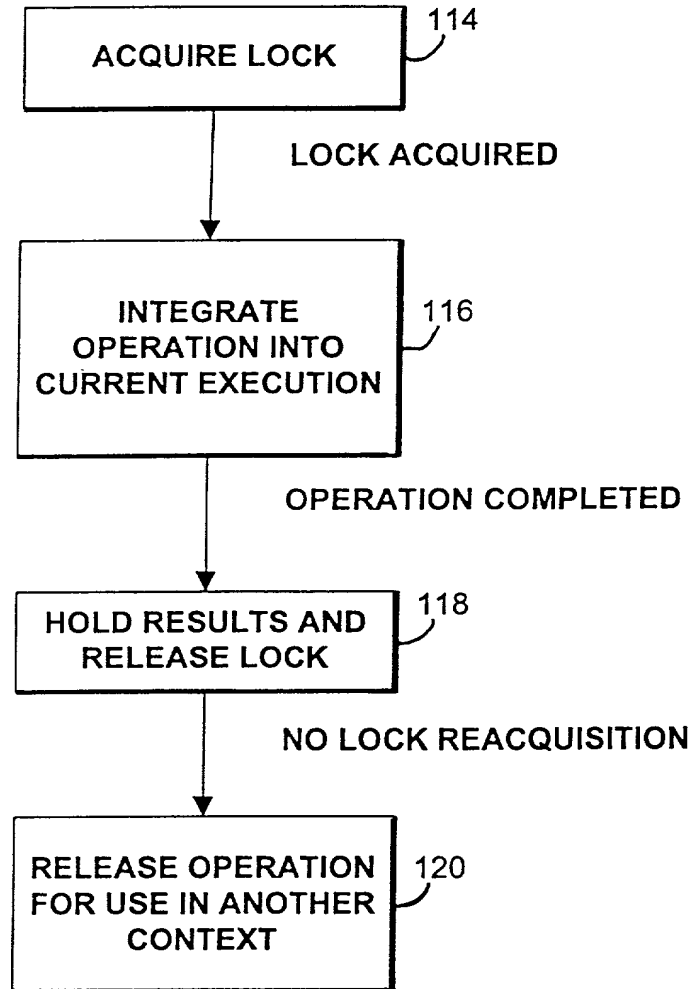


FIG. 20

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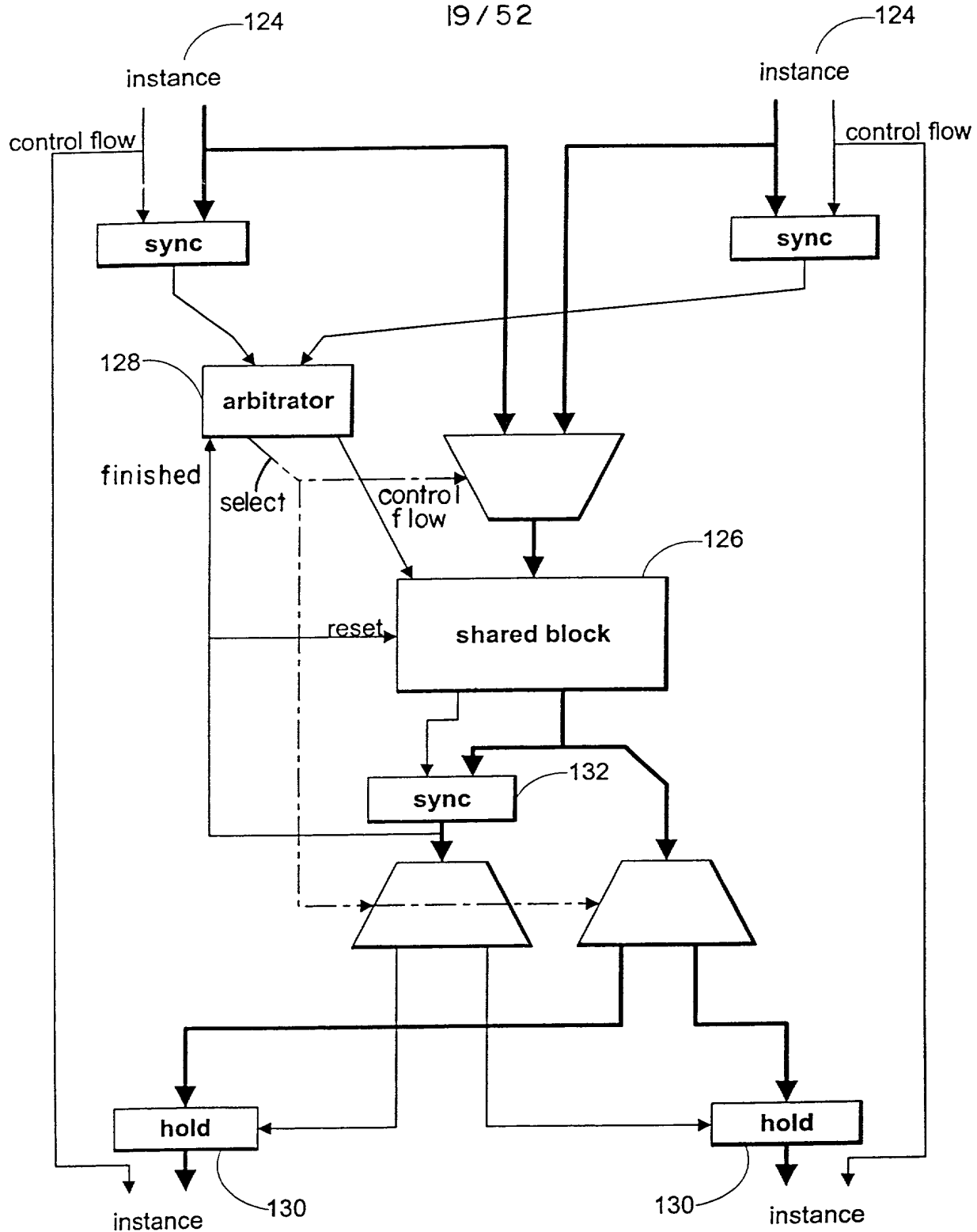
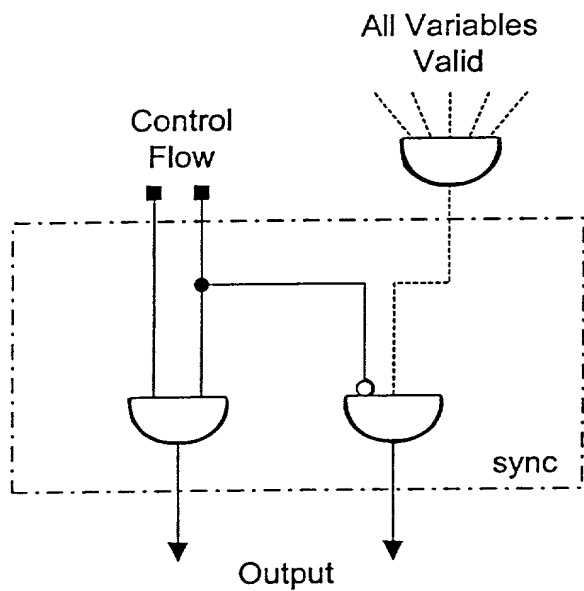


FIG. 21

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NotReady	00
SpeculateReady	01
CommitReady	11

FIG. 22

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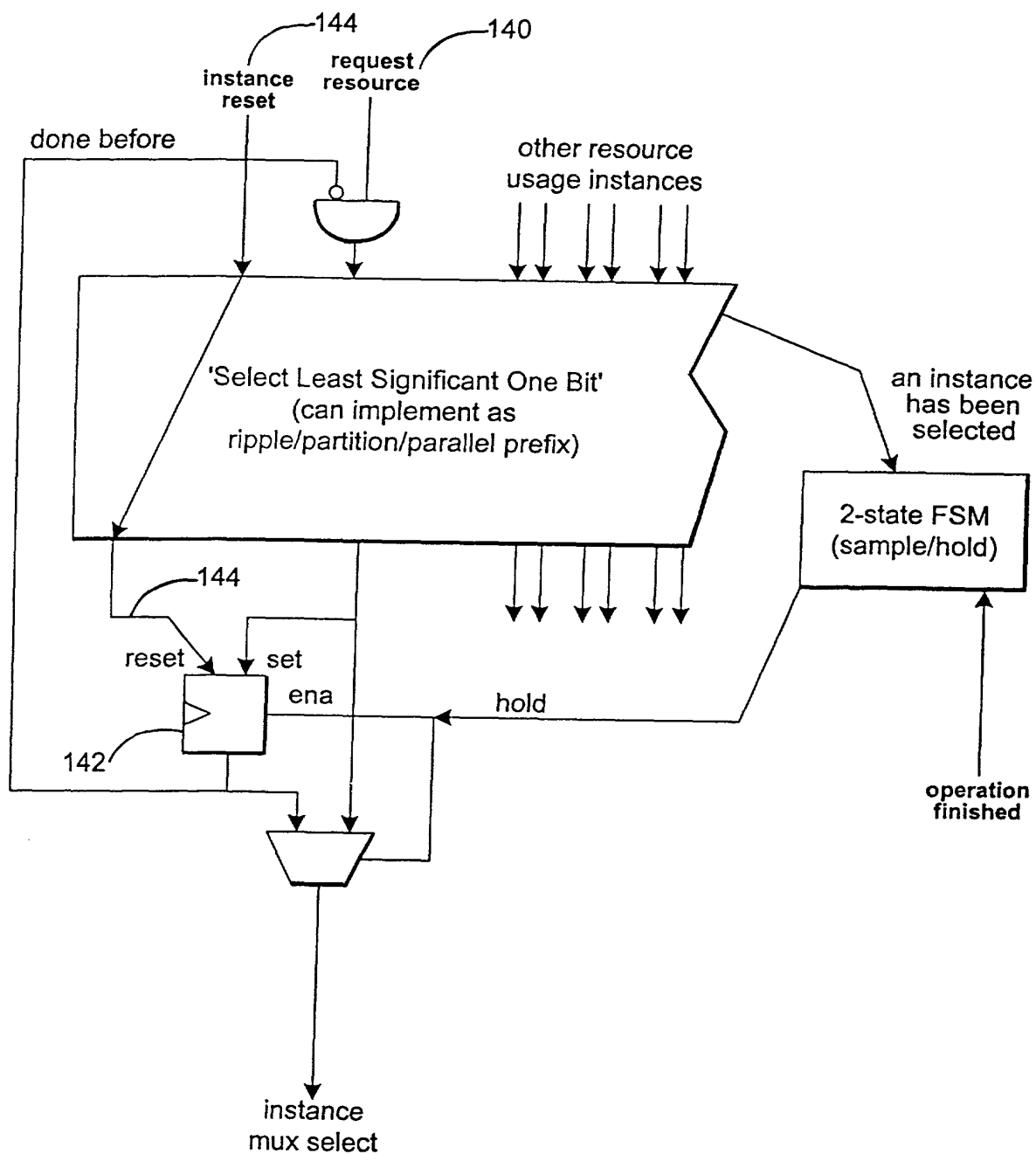


FIG. 23

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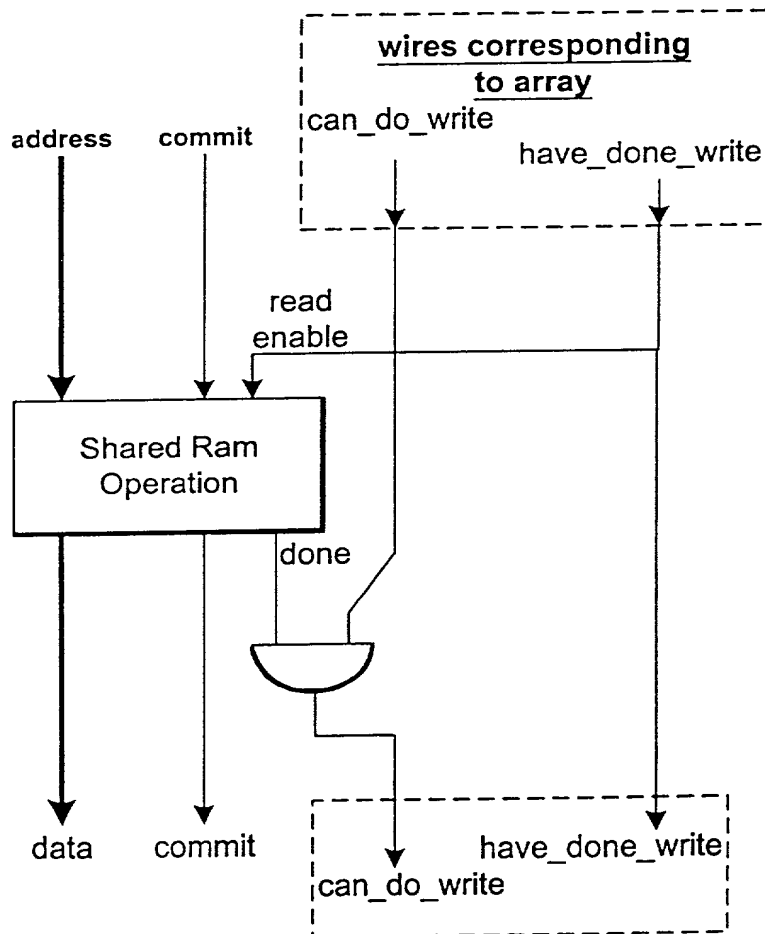


FIG. 24

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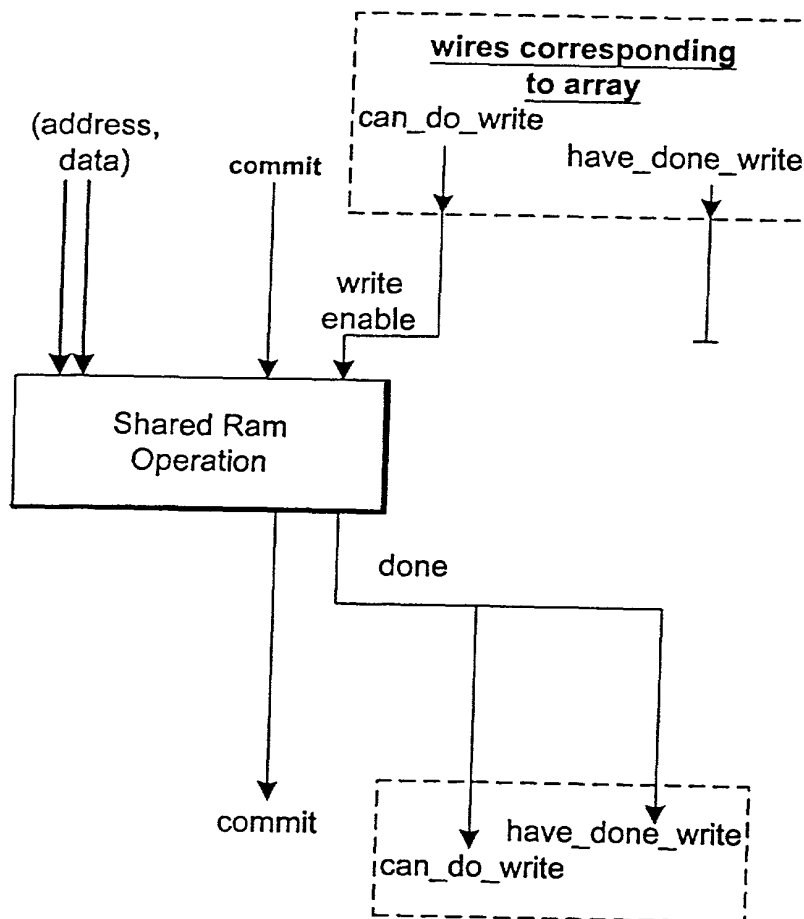


FIG. 25

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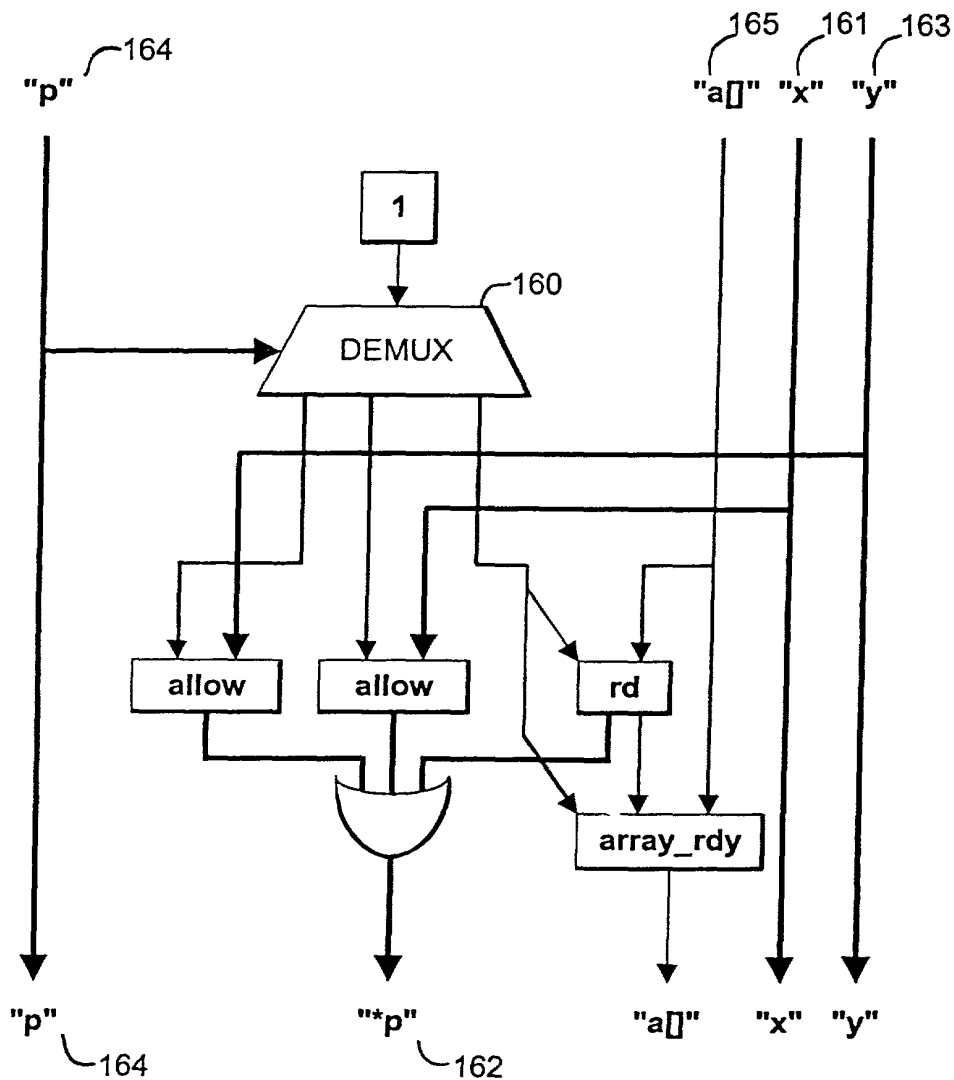


FIG. 26

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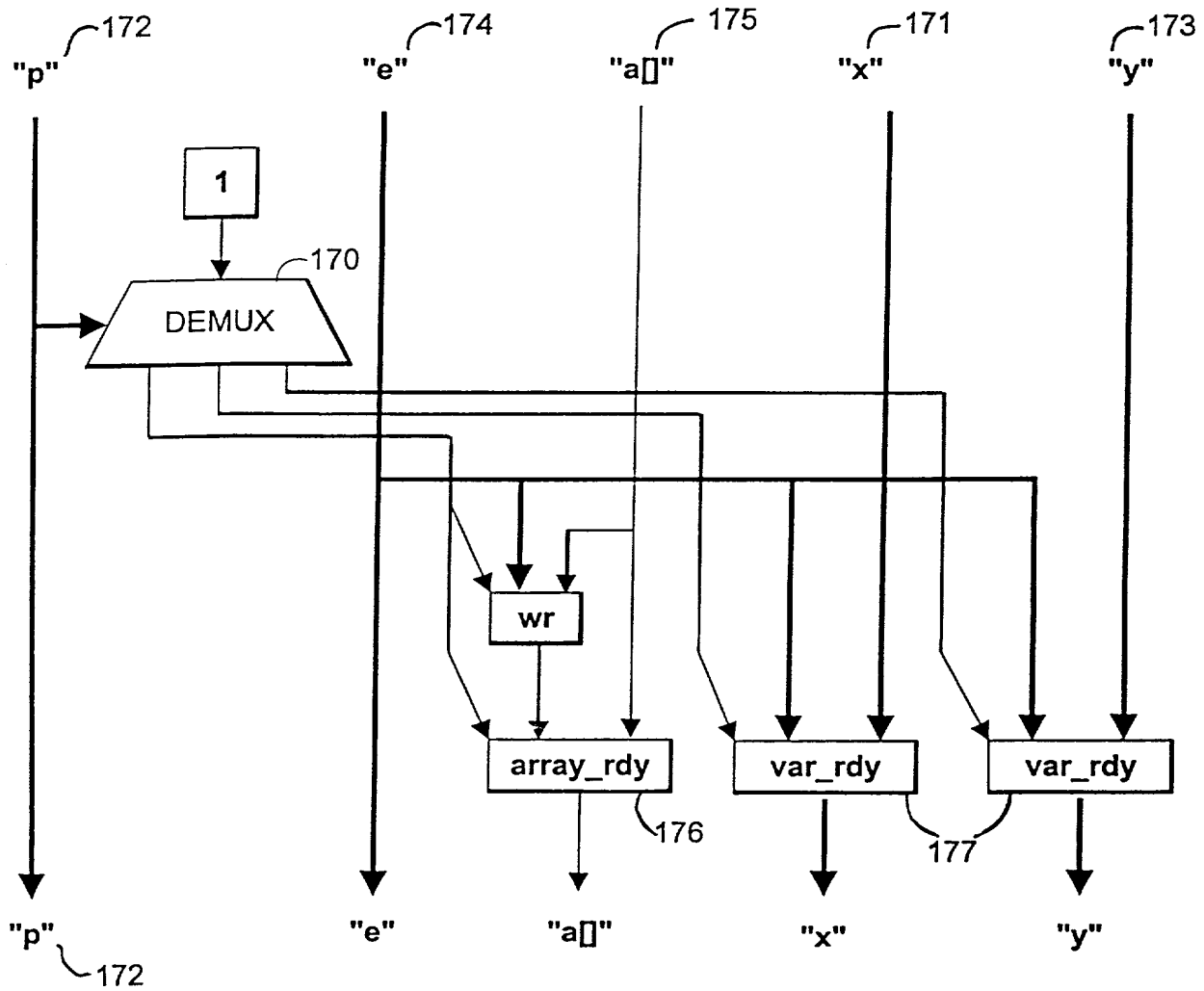


FIG. 27

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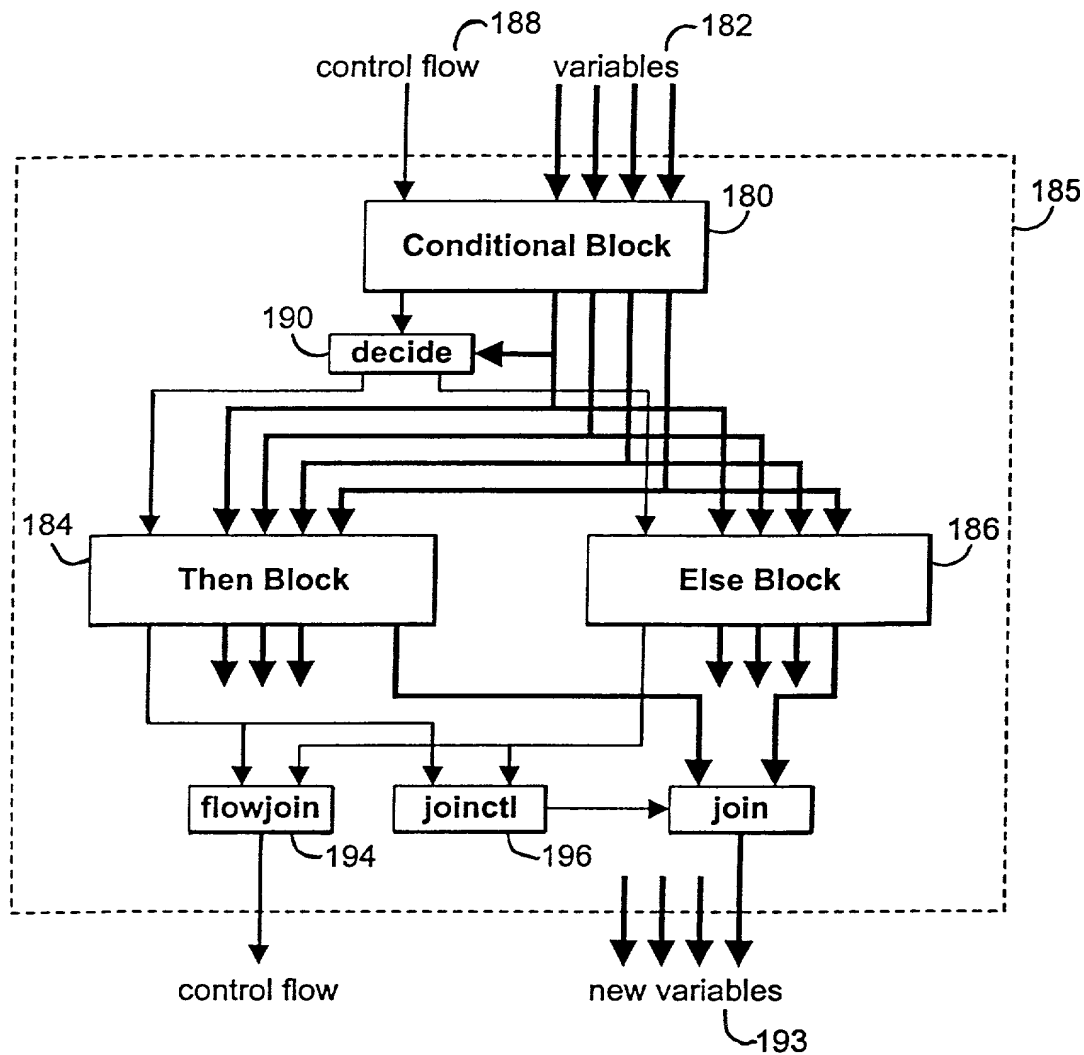


FIG. 28

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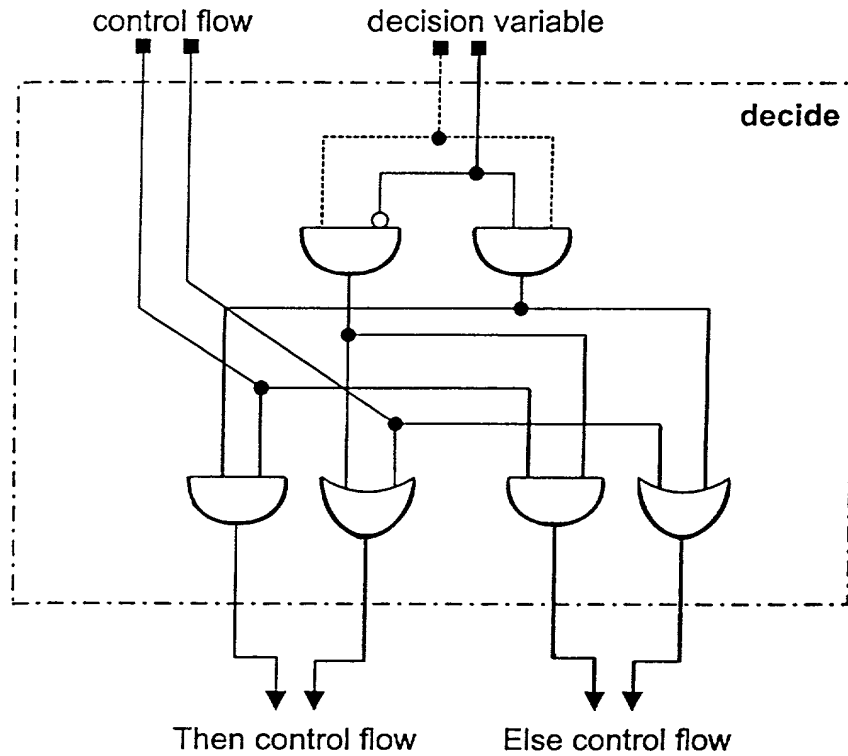


FIG. 29

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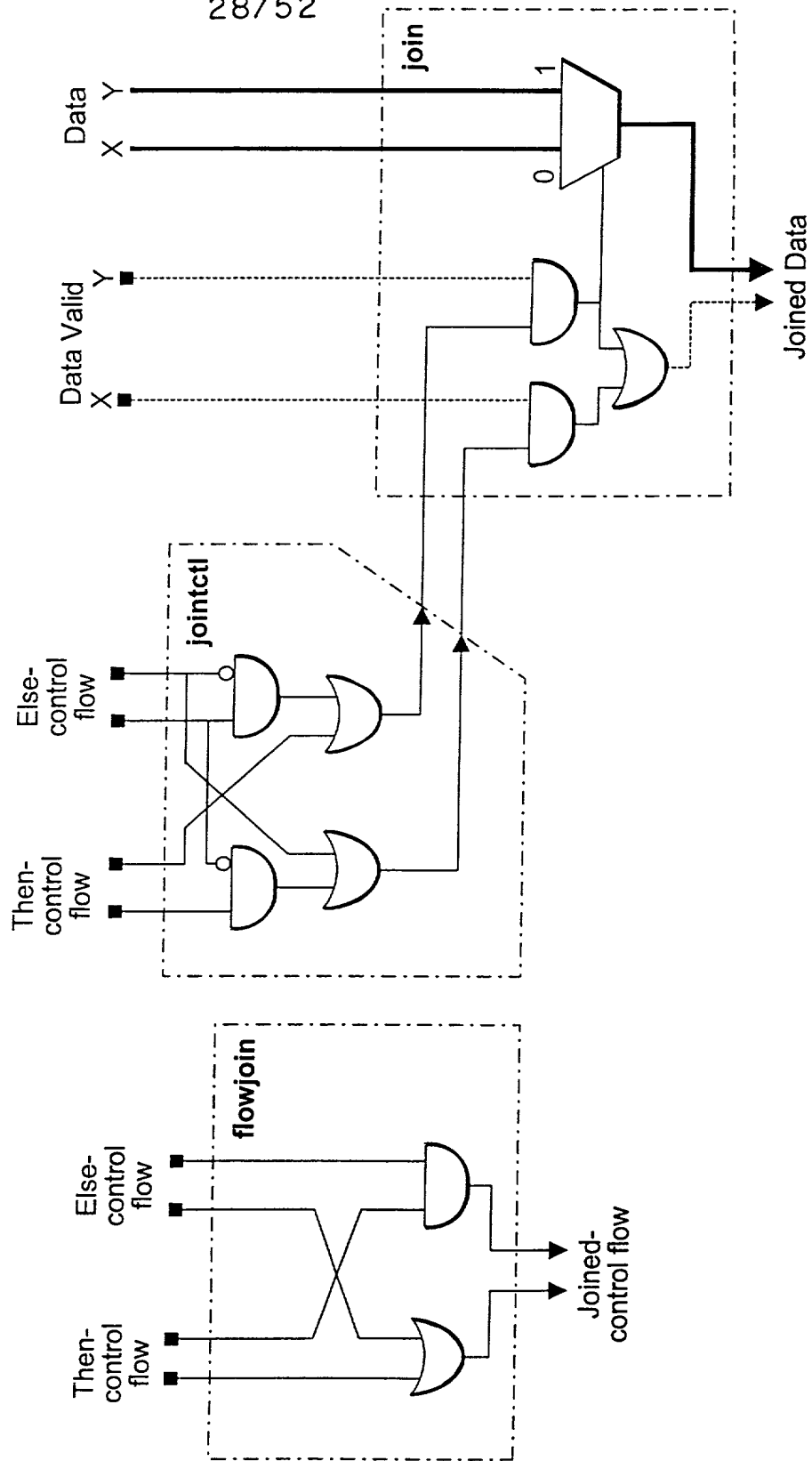


FIG. 30

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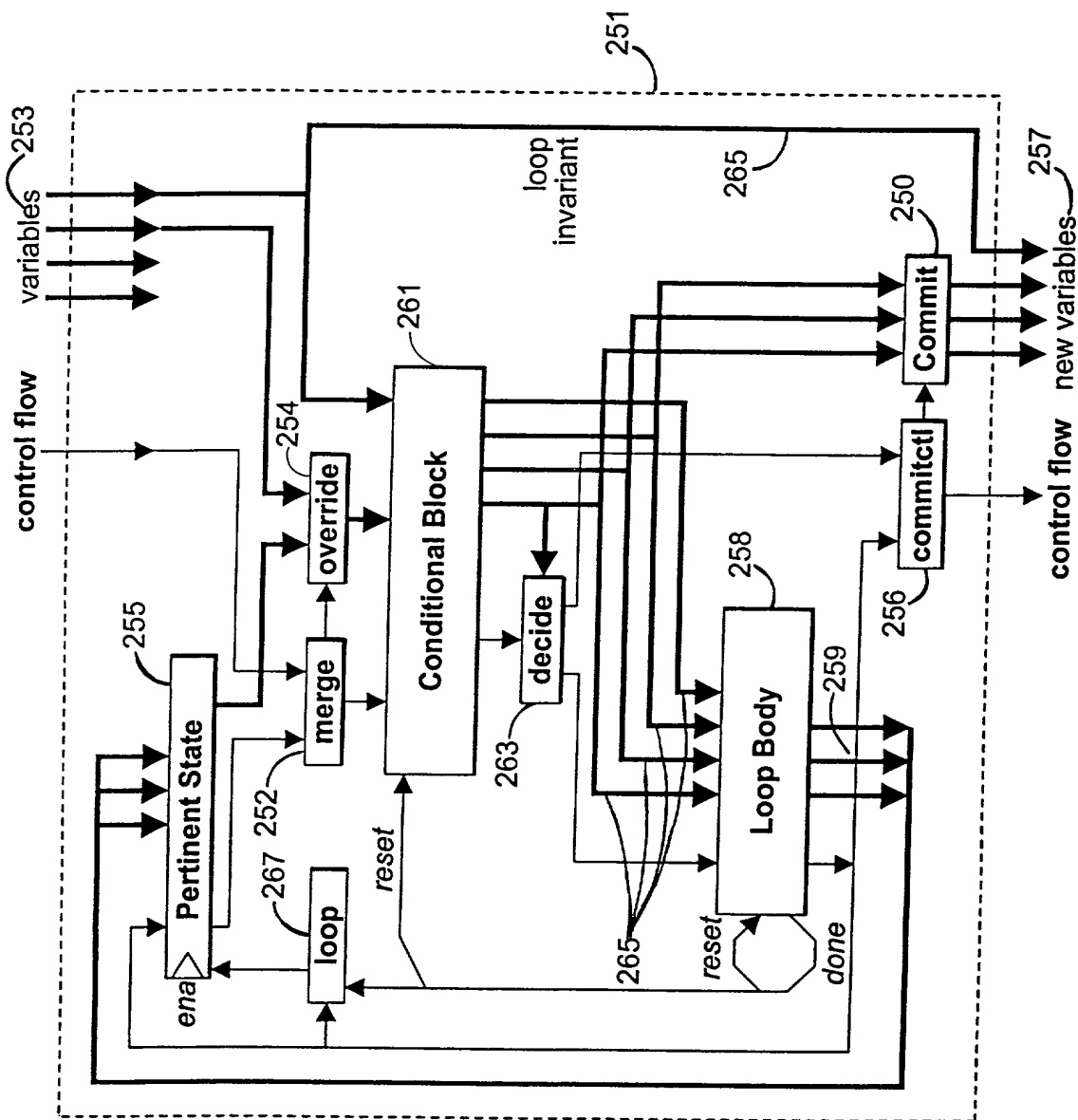


FIG. 31

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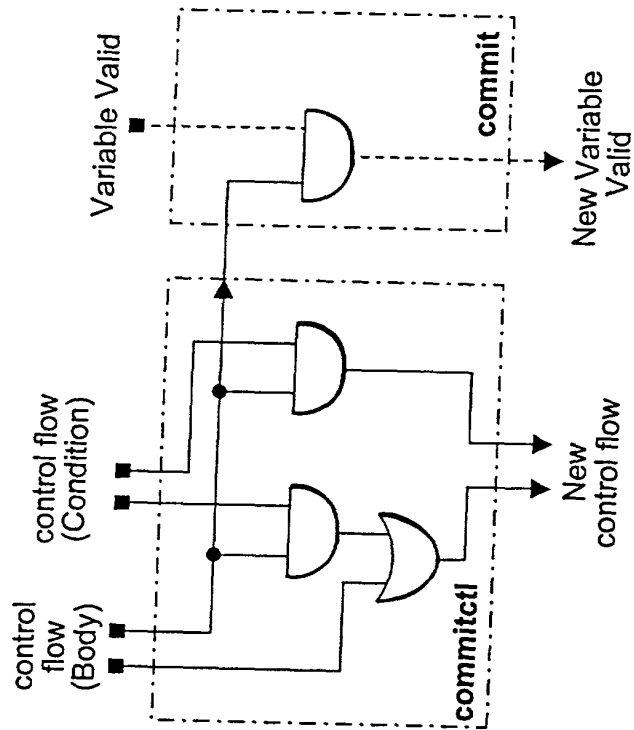


FIG. 32

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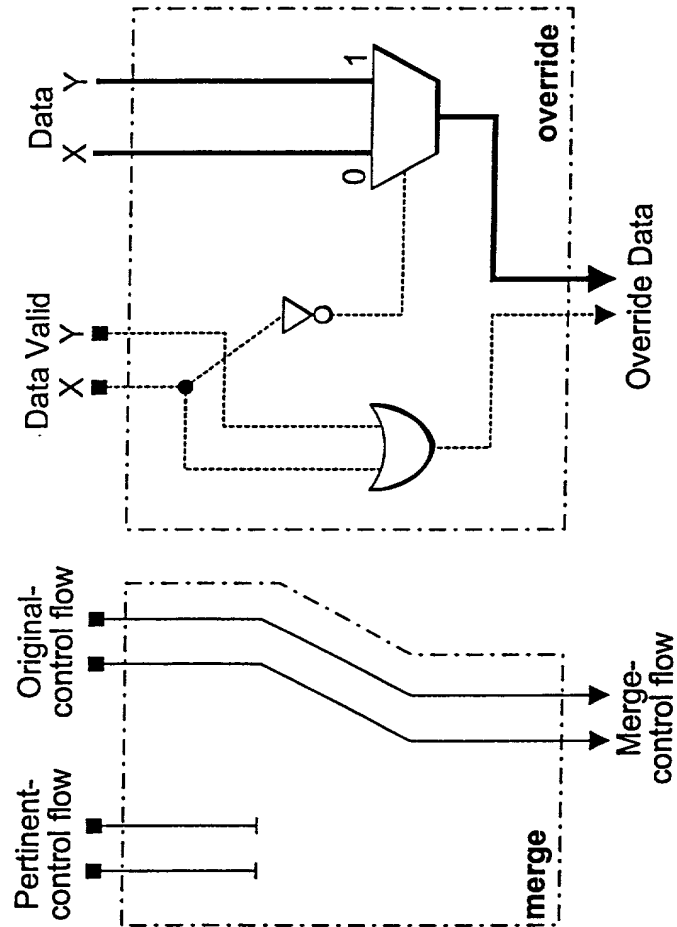


FIG. 33

FIG. 33 2/24/2001

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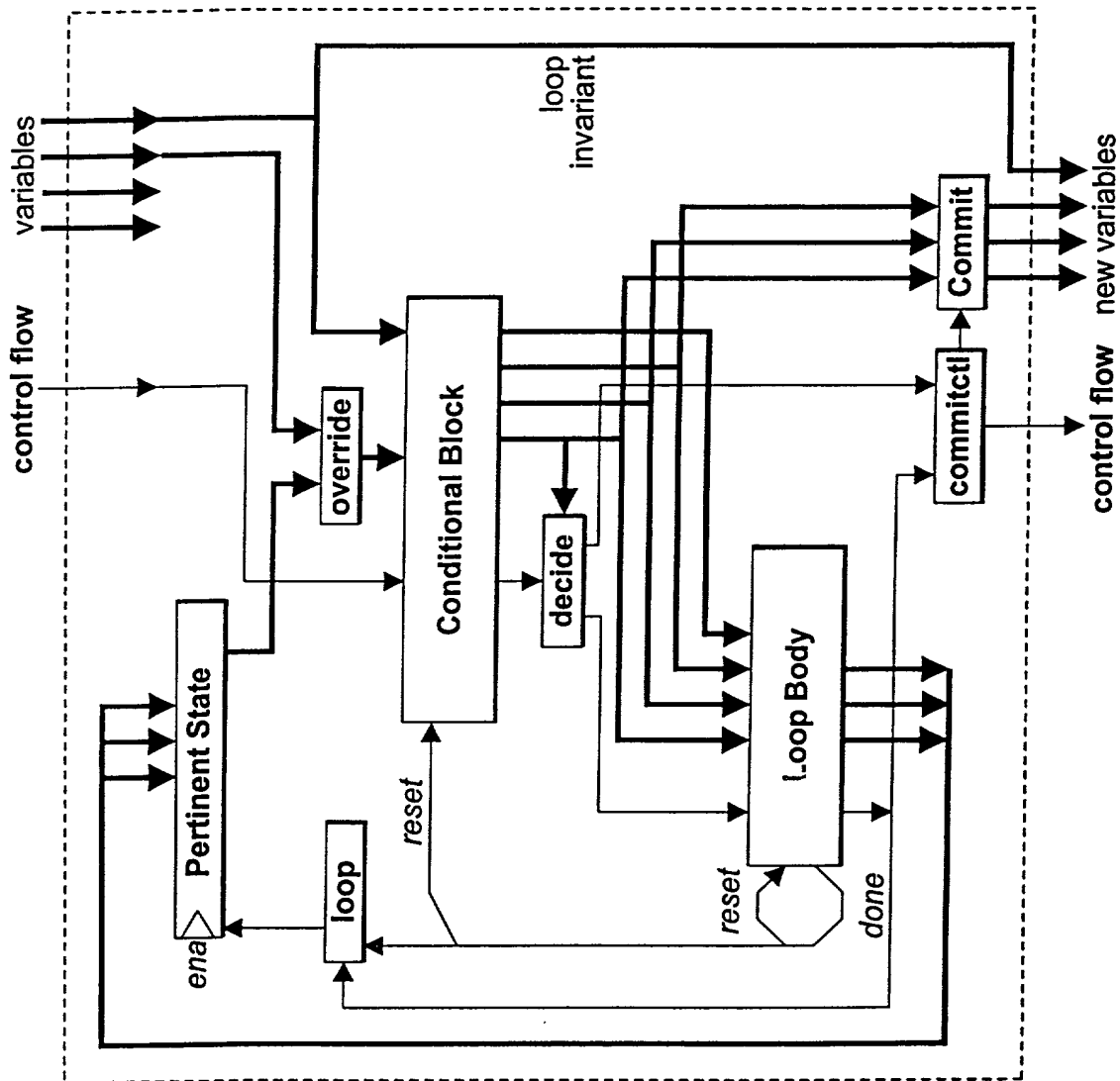


FIG. 34

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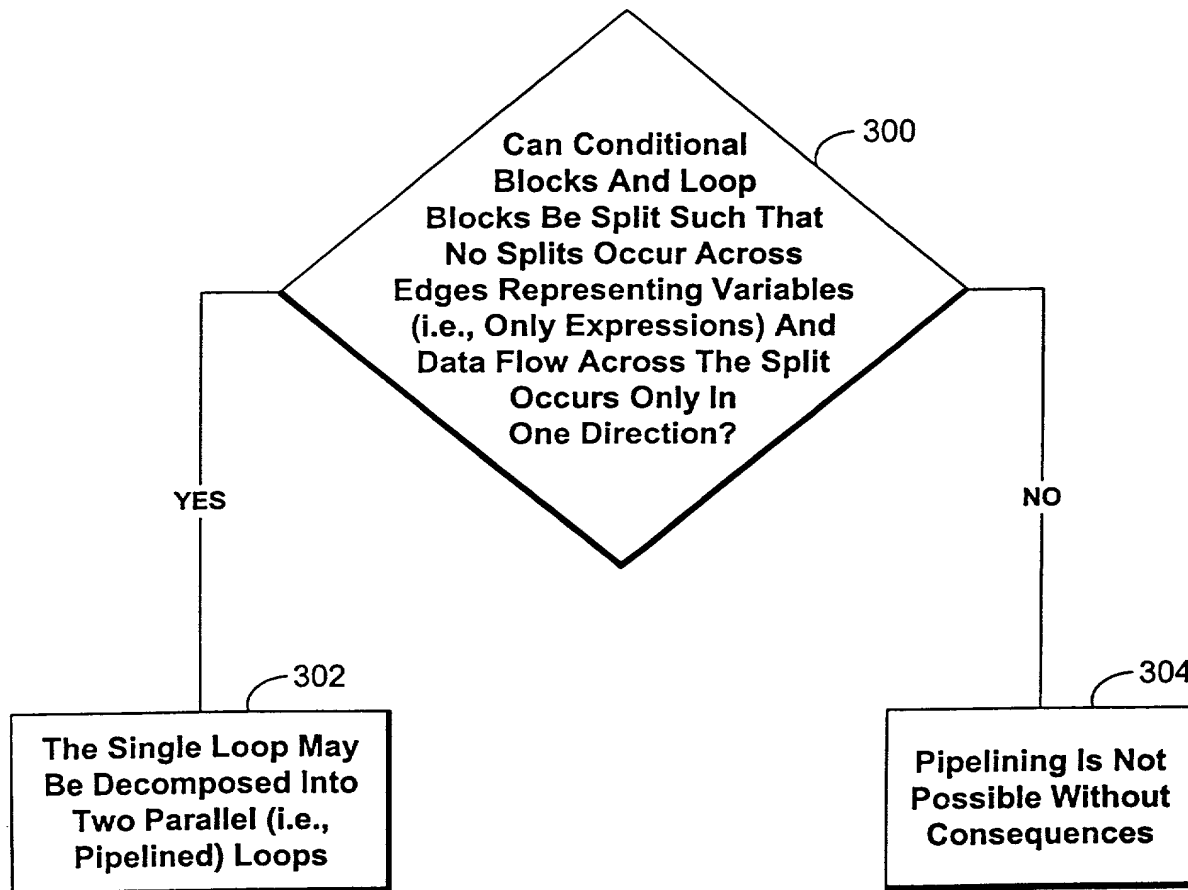


FIG. 35

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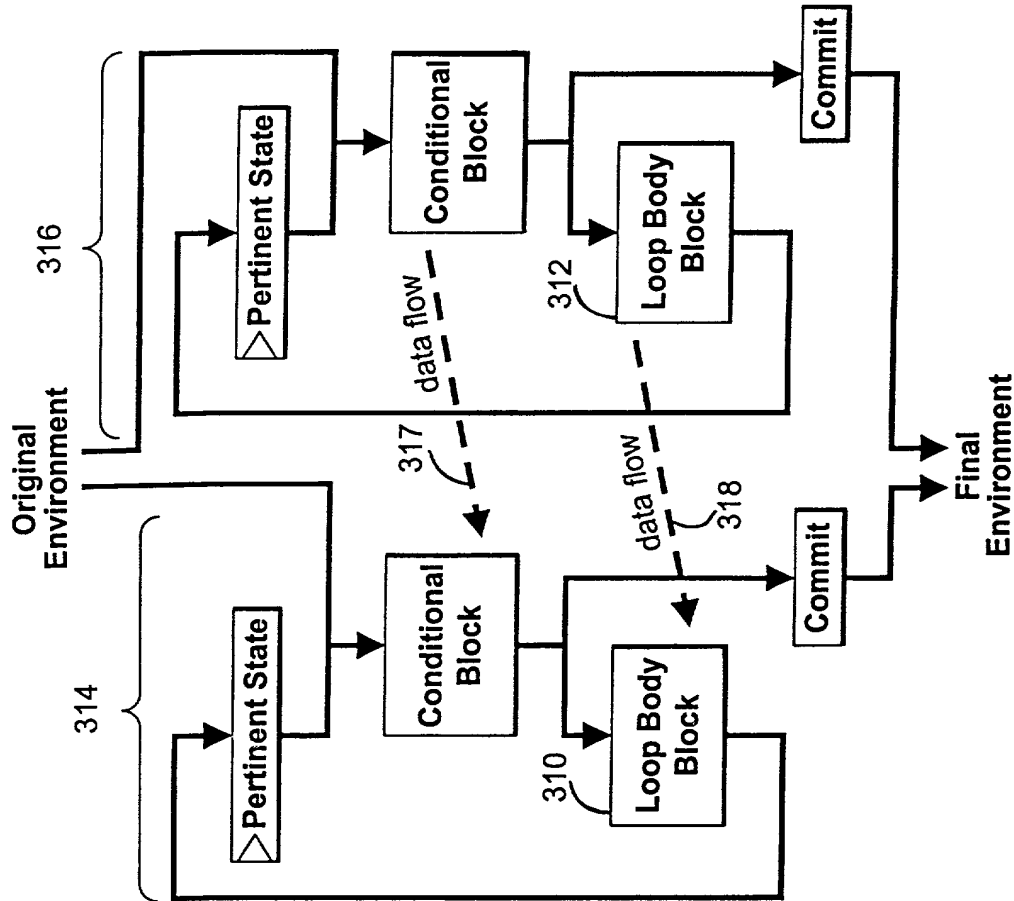


FIG. 36

FIG. 36

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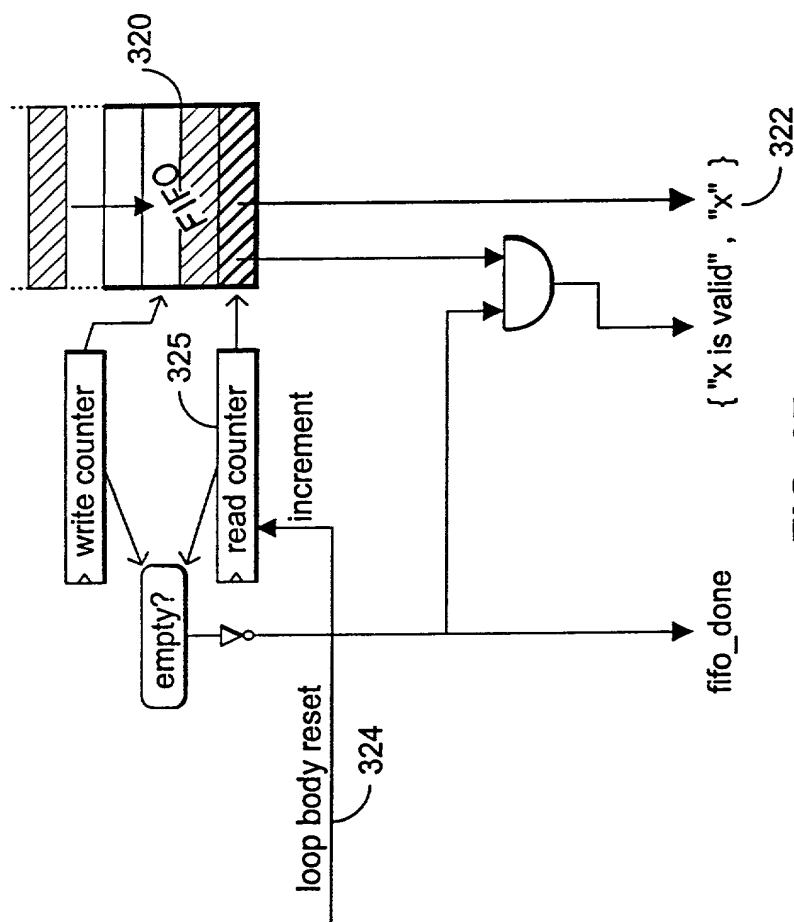


FIG. 37

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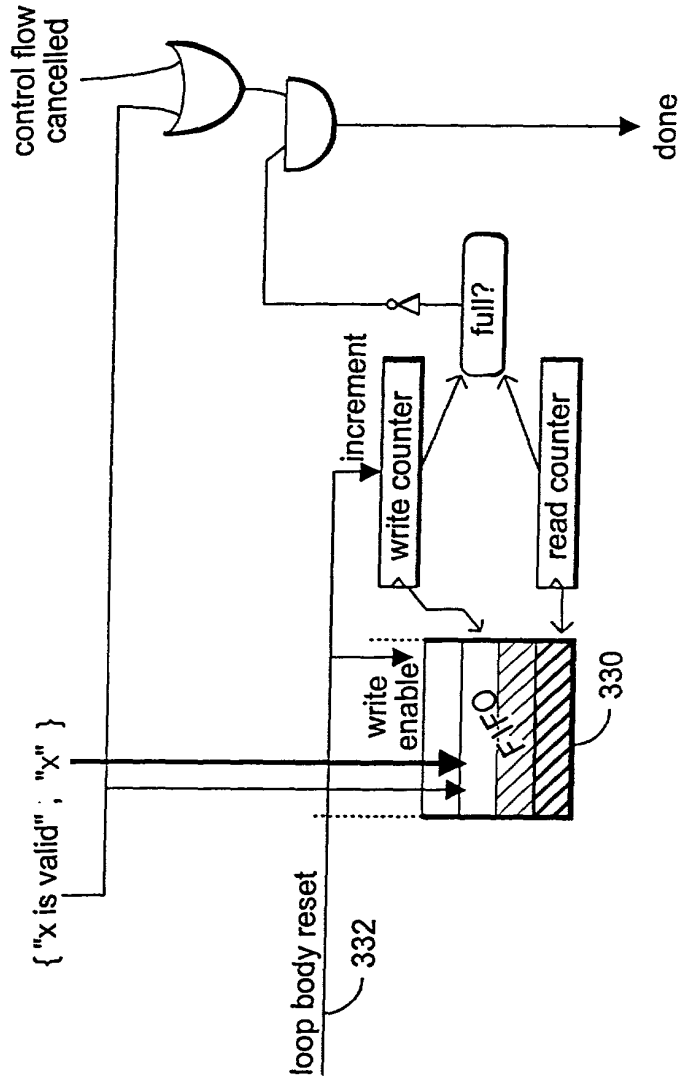


FIG. 38

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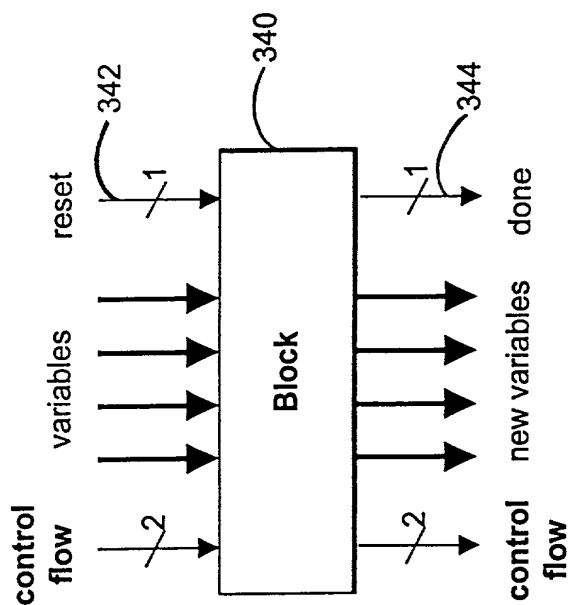


FIG. 39

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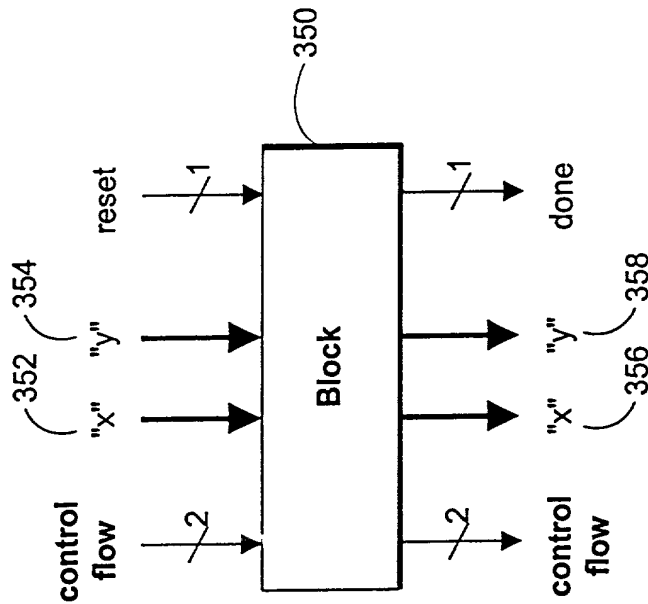


FIG. 40

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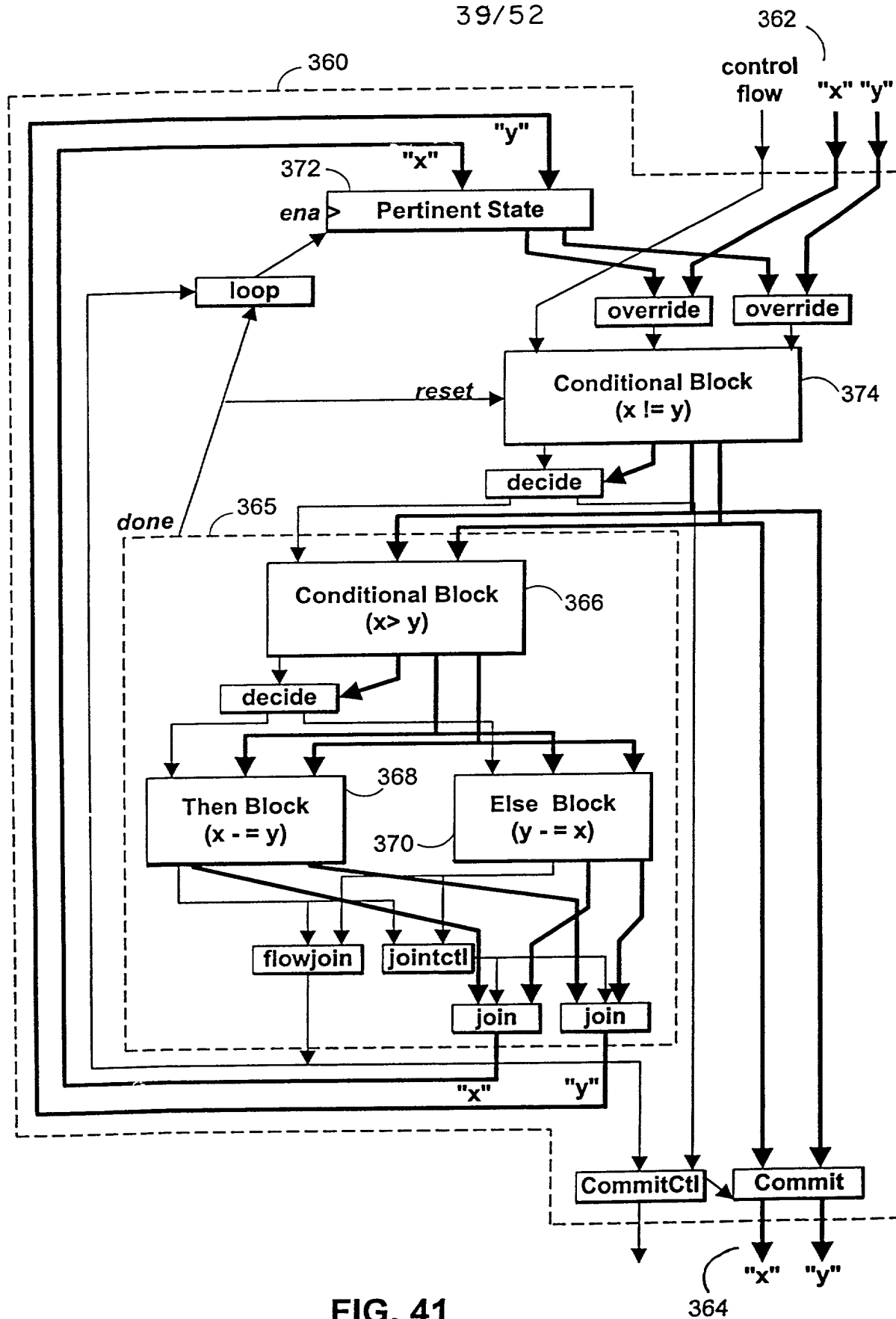


FIG. 41

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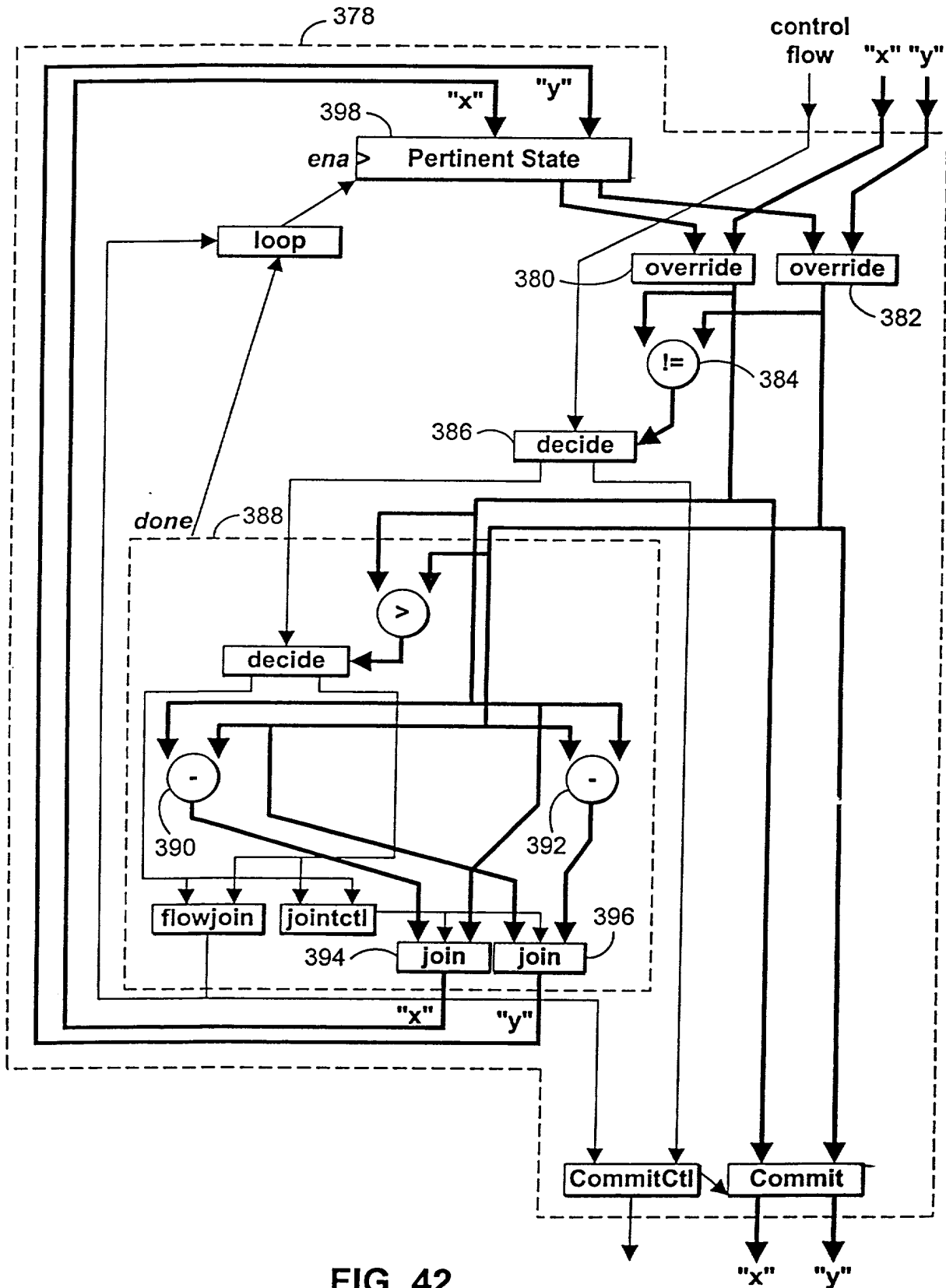


FIG. 42

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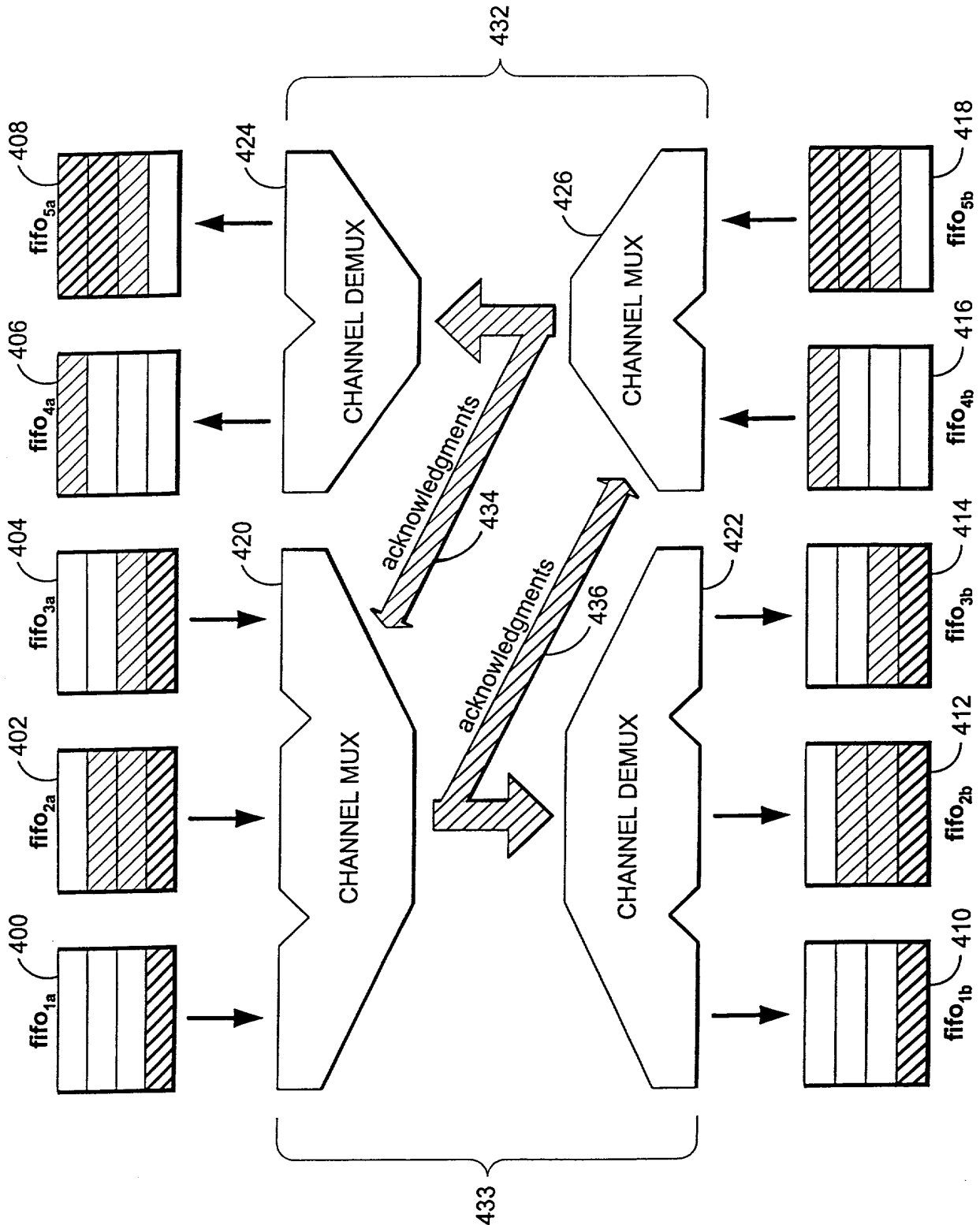


FIG. 43

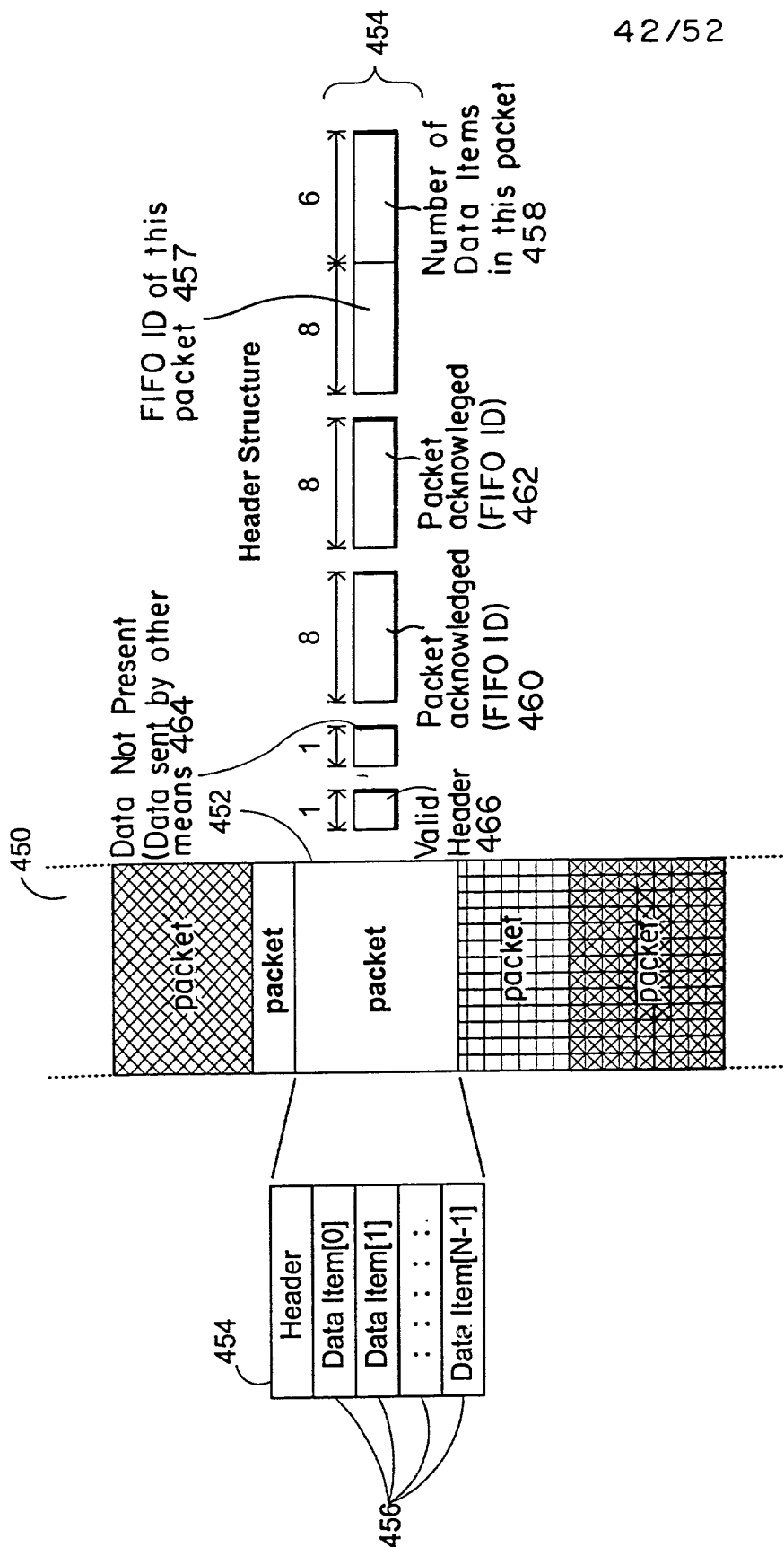


FIG. 44

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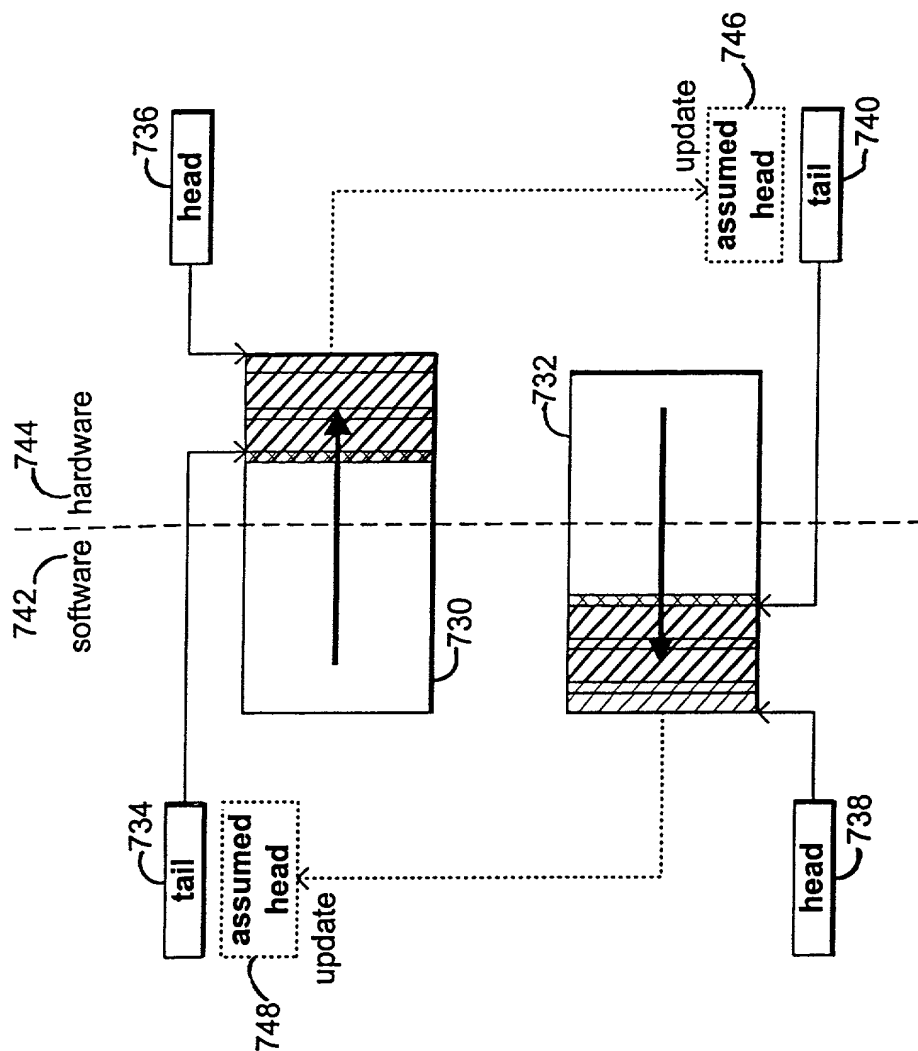


FIG. 45

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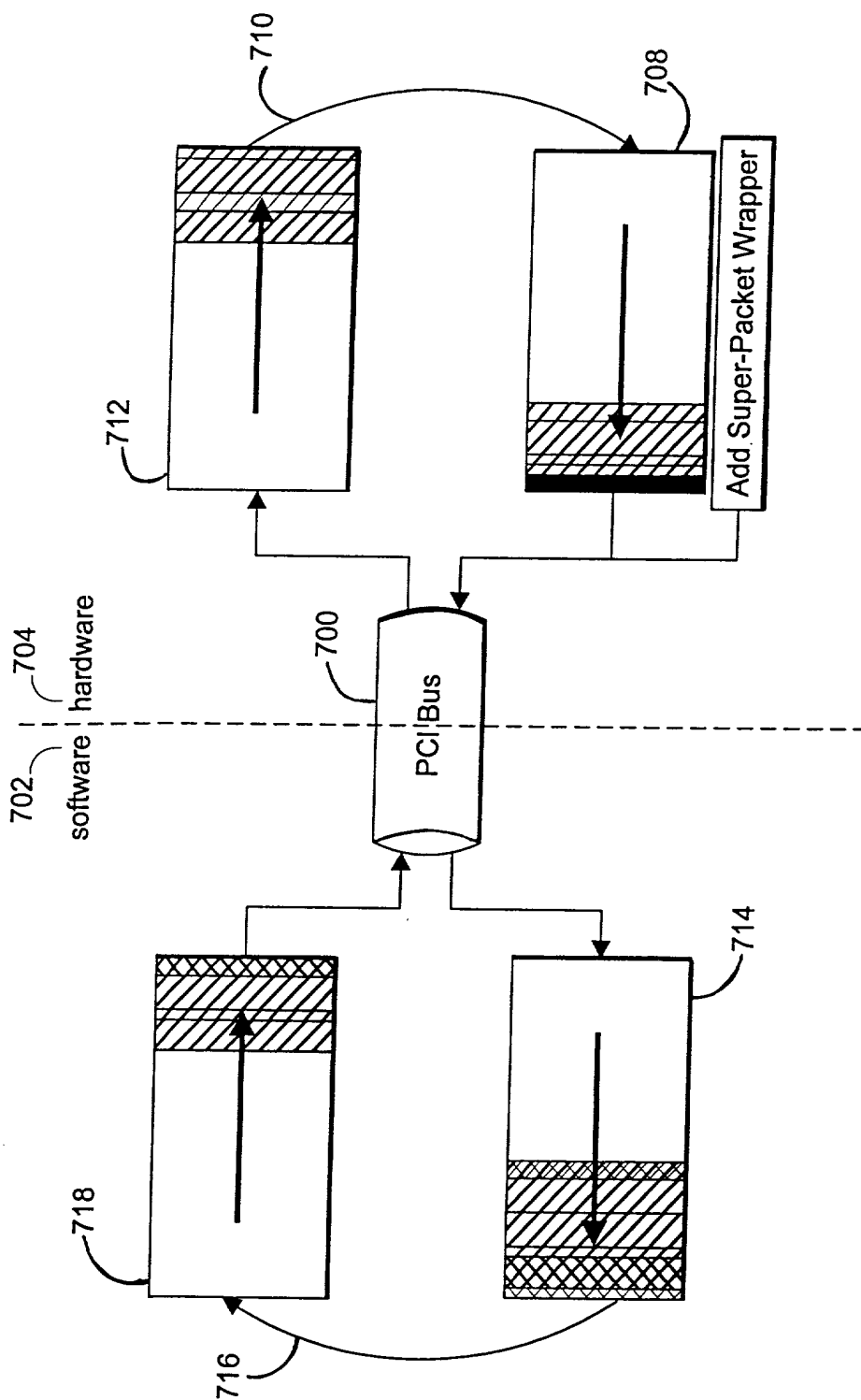


FIG. 46

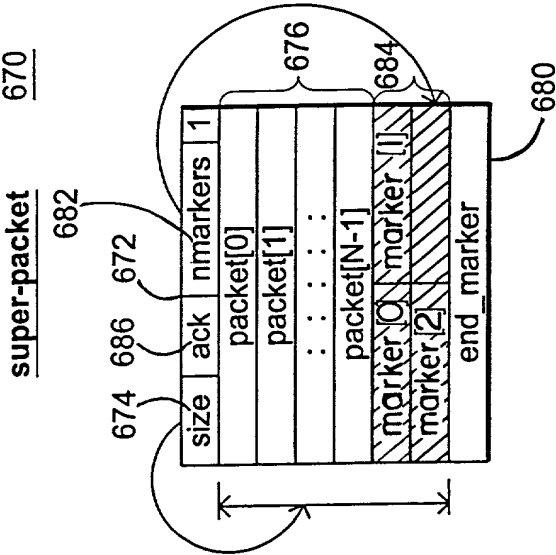


FIG. 47

Shared Memory

Processor's Local Memory

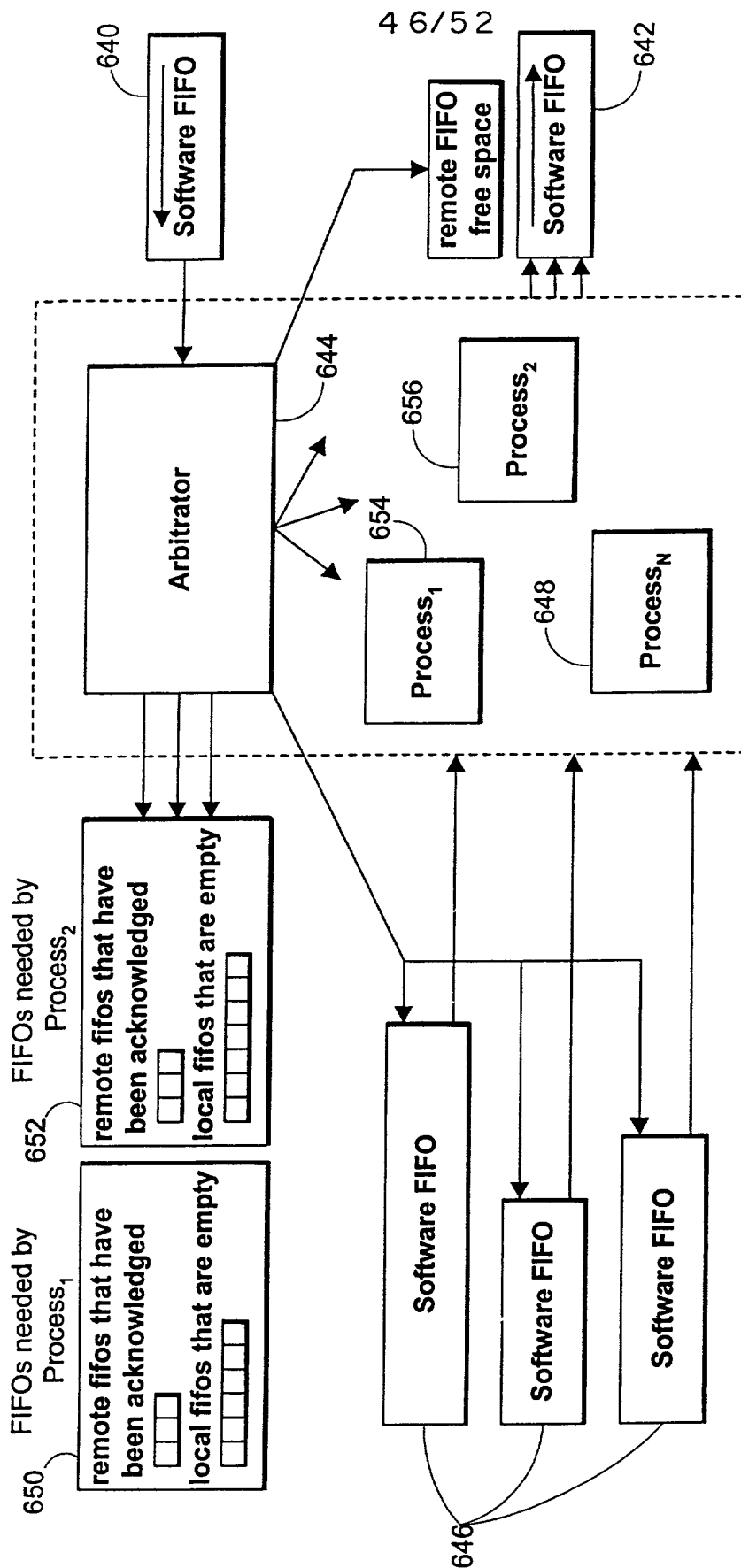


FIG. 48

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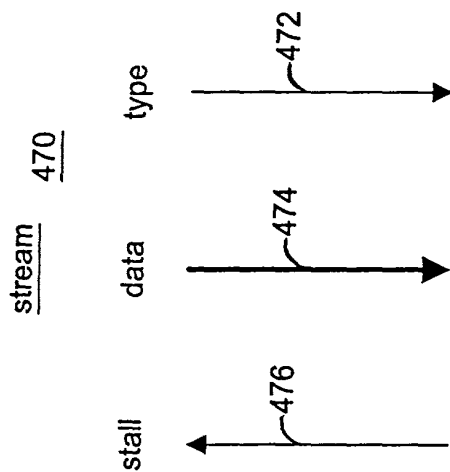


FIG. 49

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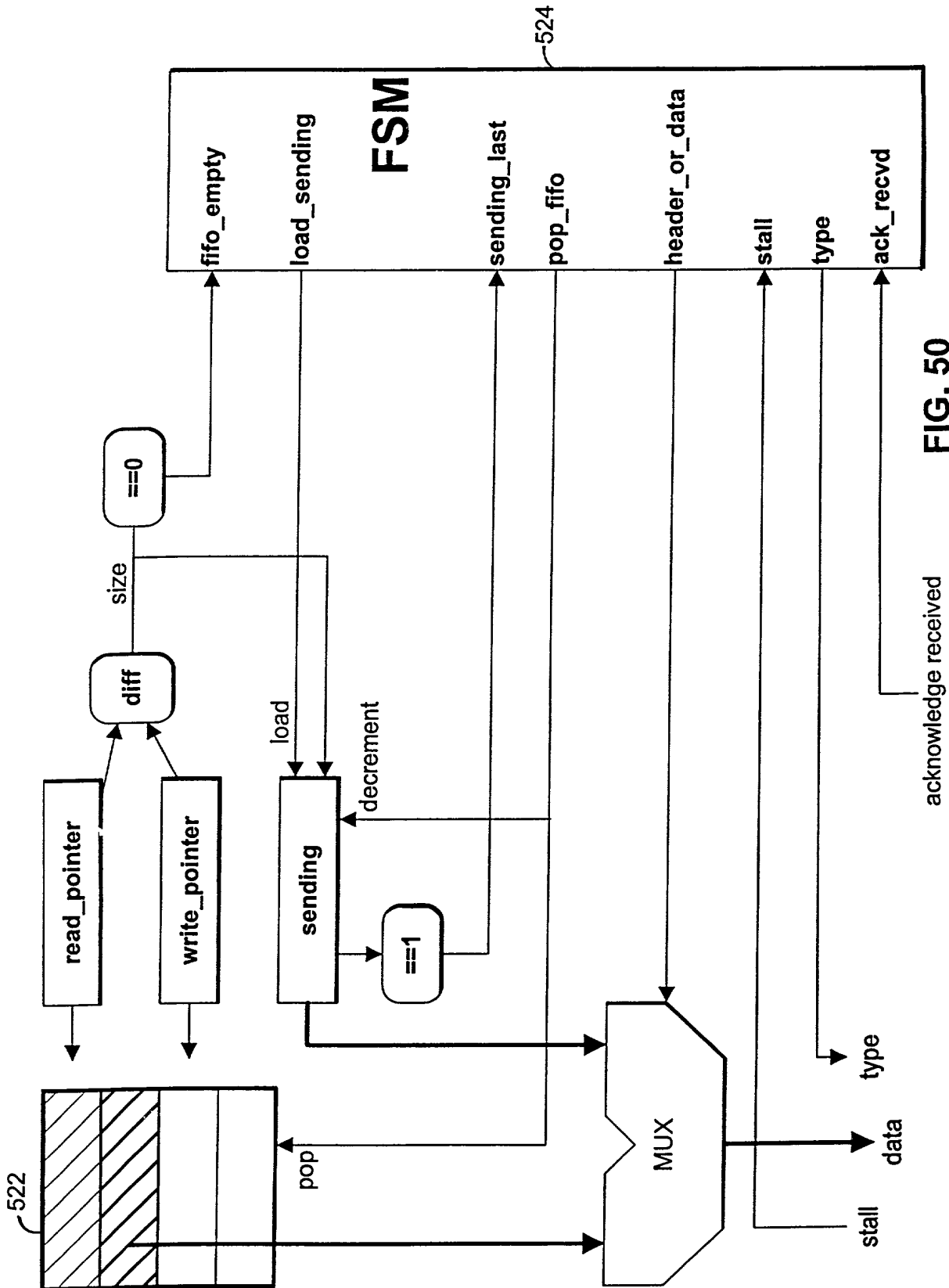


FIG. 50

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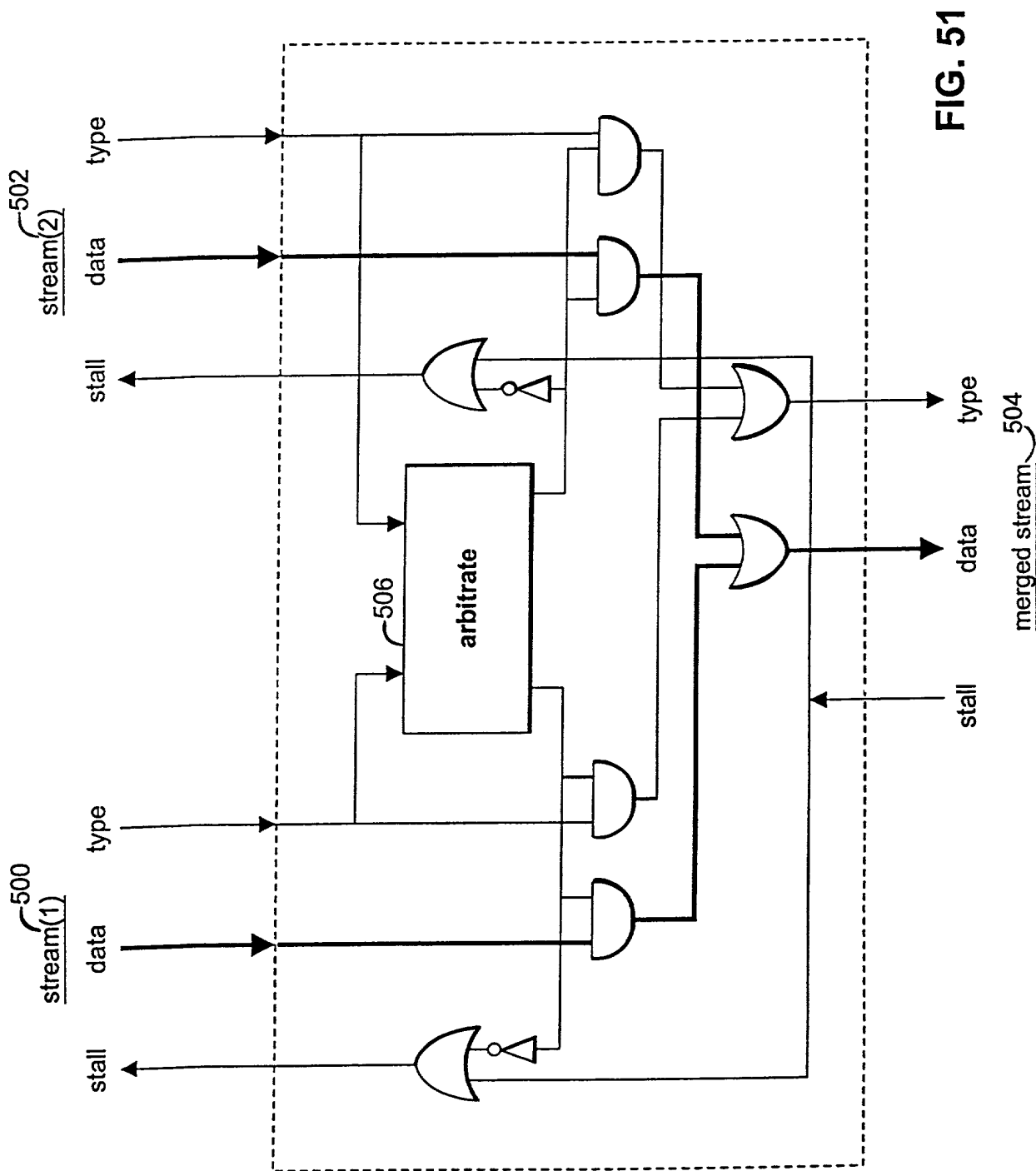


FIG. 51

FIG. 51 2/24/2001

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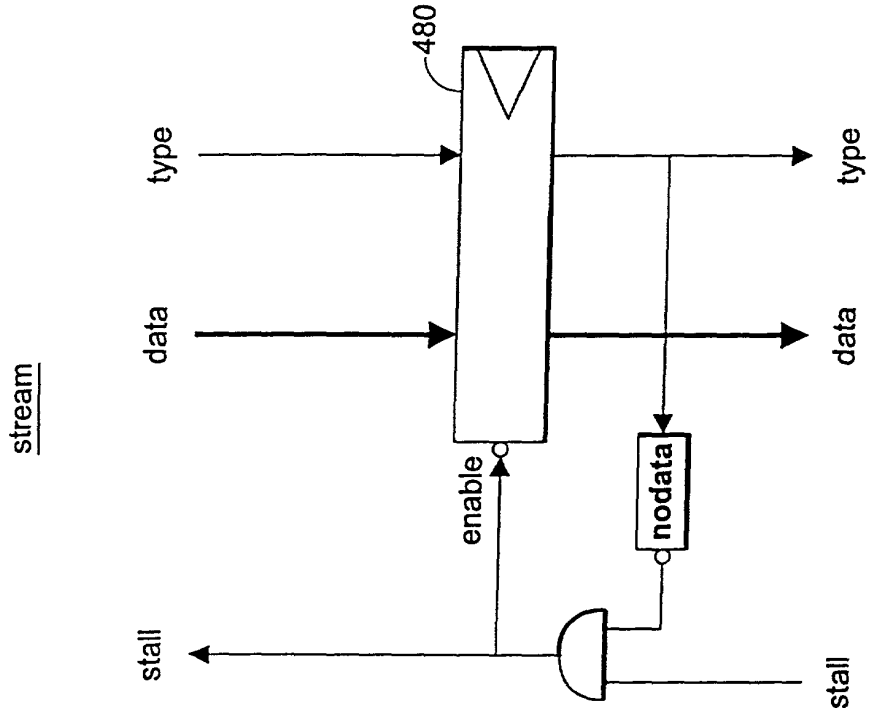


FIG. 53

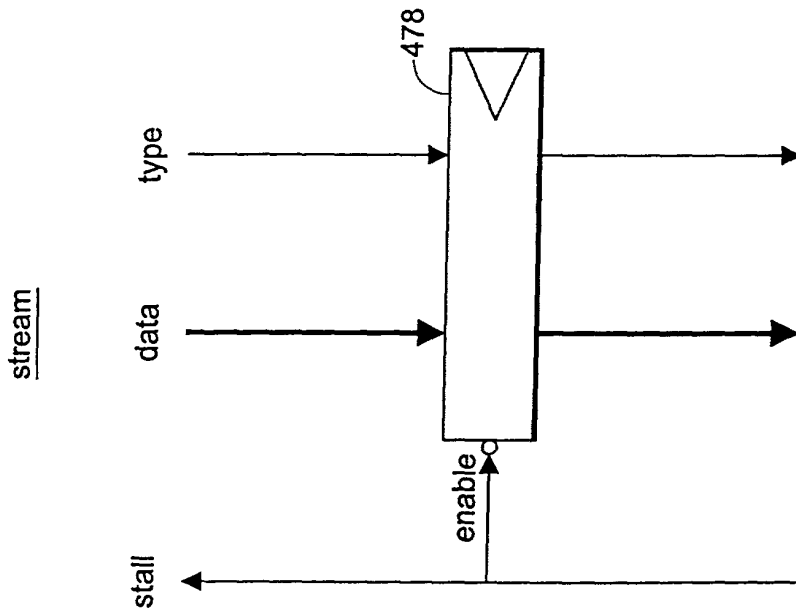


FIG. 52

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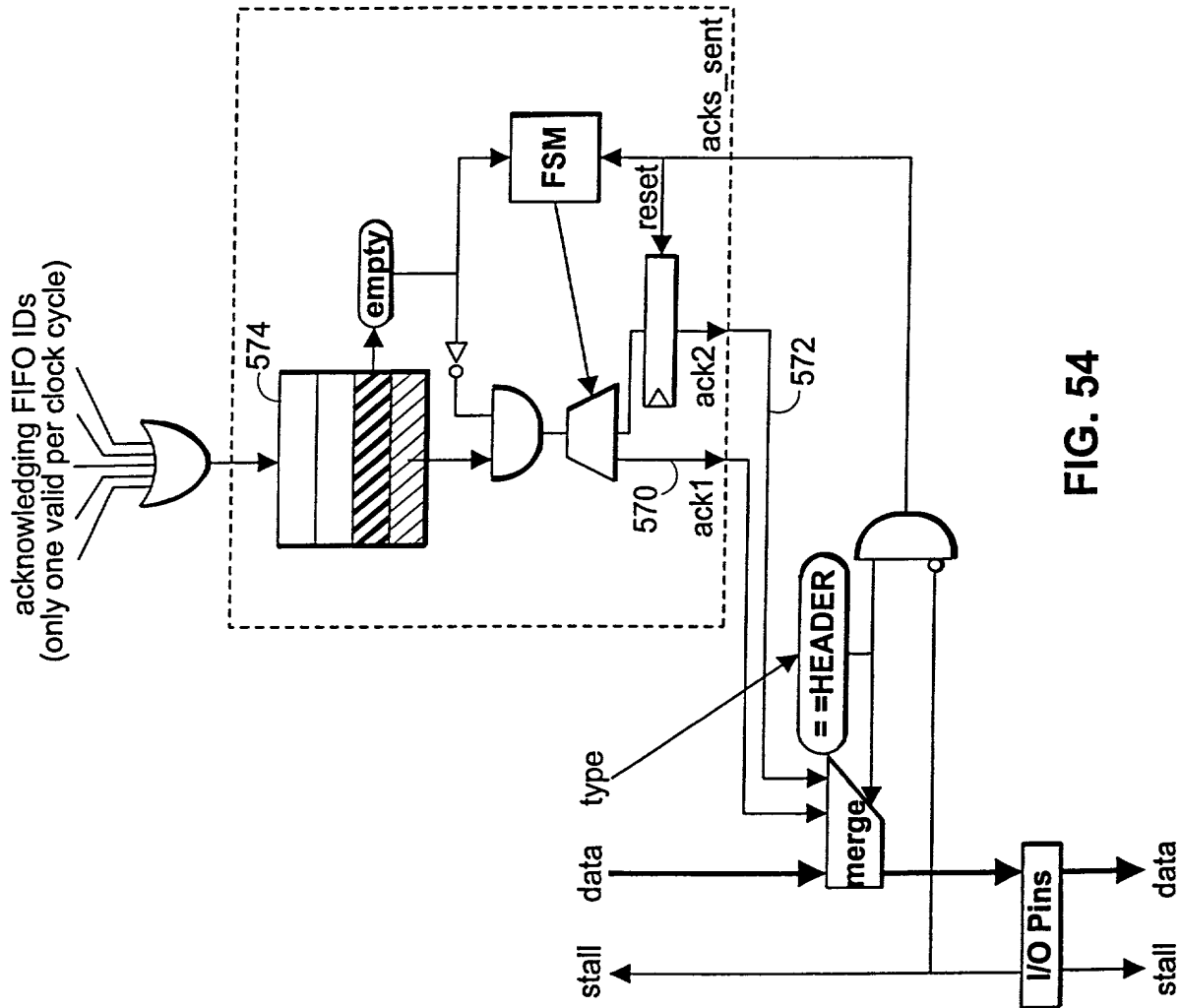


FIG. 54

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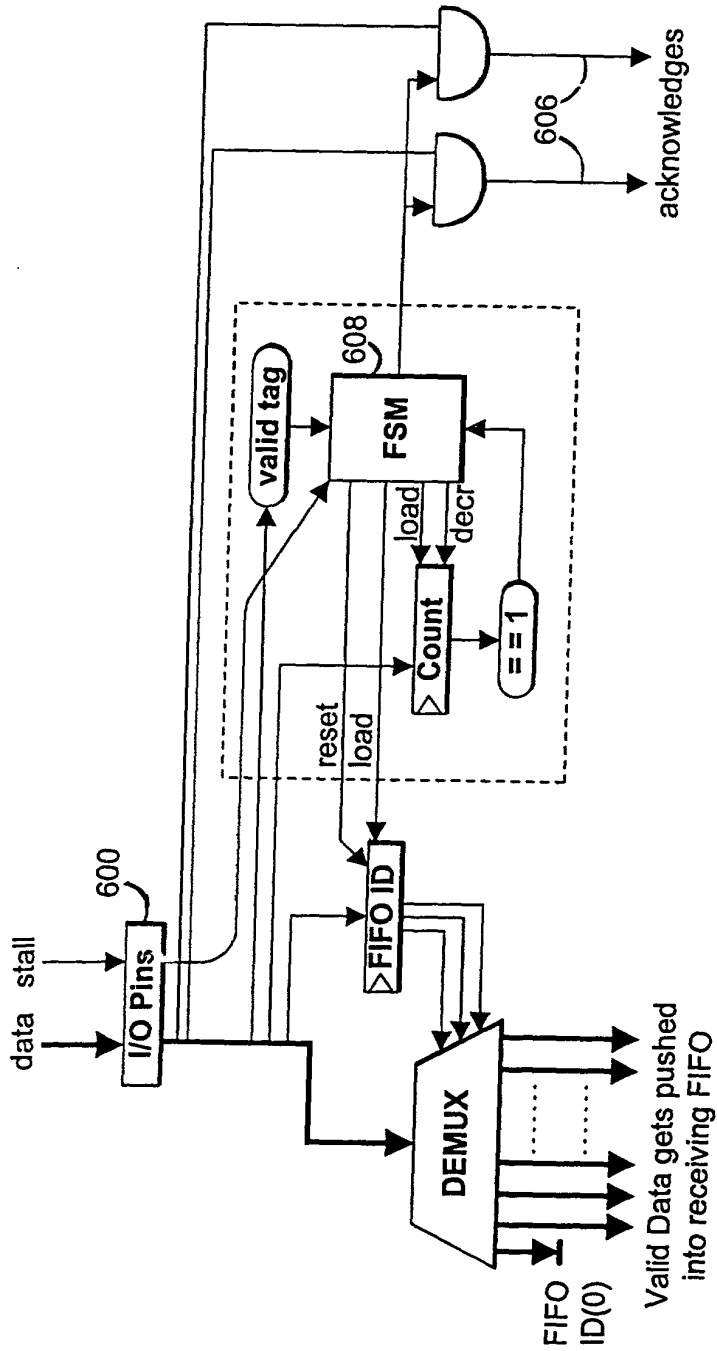


FIG. 55